

Model Name: GA-B250M-EVO

rev 1.0

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1151-A
05	CPU_LGA1151-B-DDR4
06	CPU_LGA1151-C
07	CPU_LGA1150-D
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09	DDR4 CHANNEL B
10	PCH_CLK BUFFER
11	PCH_DMI,USB,PCIE
12	PCH_MISC
13	PCH SATA,PCIE,SATA_EXPRESS
14	PCH PWR
15	PCH GND
16	ITE 8686 LPC IO
17	HWM
18	FAN CTRL--SIO
19	PCI EXPRESS*16 SLOT
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21	Single BIOS
22	M.2 X4 (Q)
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SHEET

TITLE

29	ISL95858 VCCGT-IRON
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33	RT8237_PCH-BEAD
34	DISCRETE POWER
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41	USB30_LAN CONNECTOR-8111G
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43	REAR AUDIO JACK
44	ADUIO LED
45	R_USB30_1
46	R_USB30_2
47	F_USB30
48	F_USB
49	F_PANEL
50	COM, TPM
51	EMI-ESD
52	POWER MAP
53	NTC MAP
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55	
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Gigabyte Technology

Title			Cover Sheet
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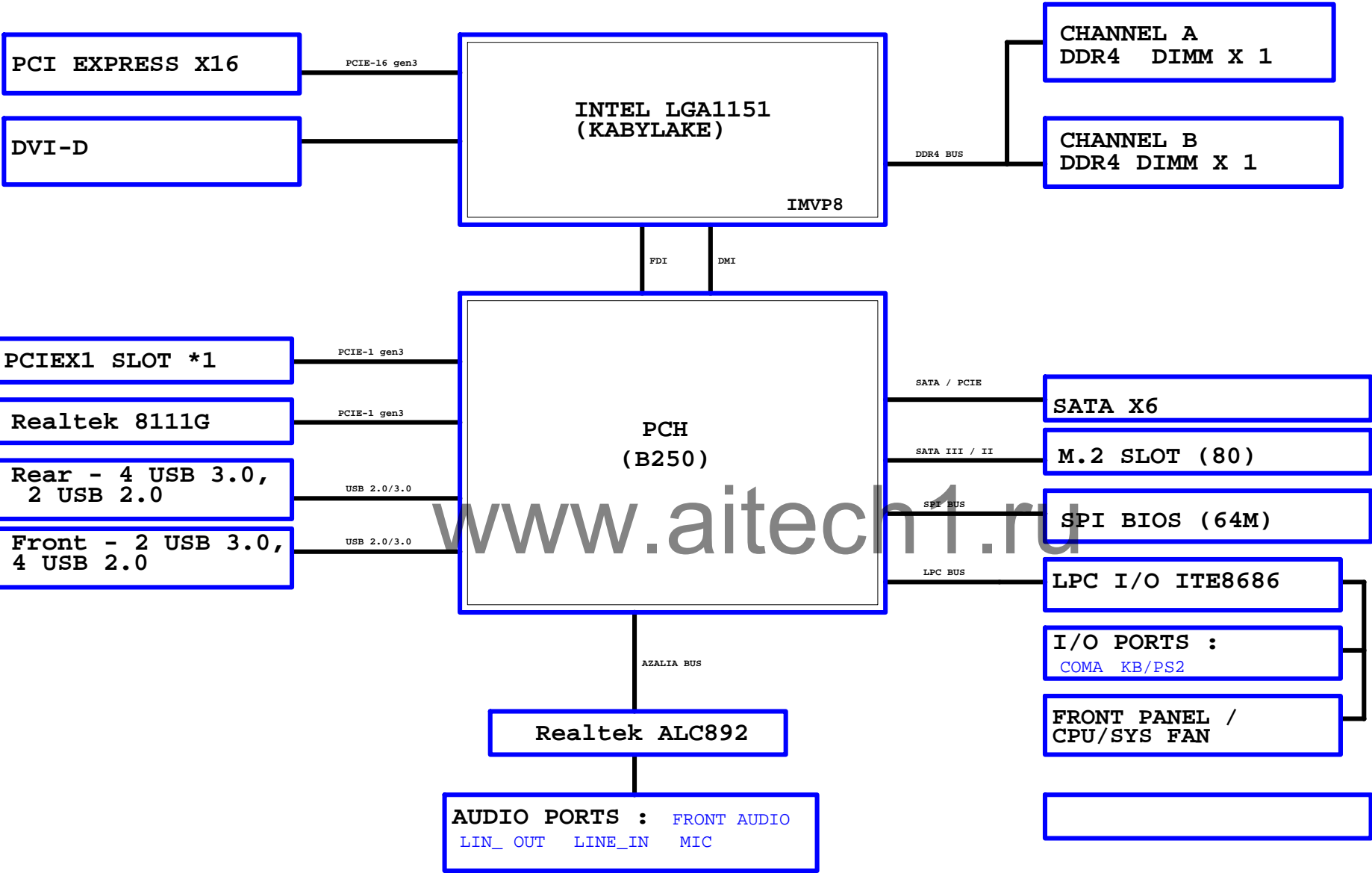
rev1.0

Circuit or PCB layout change

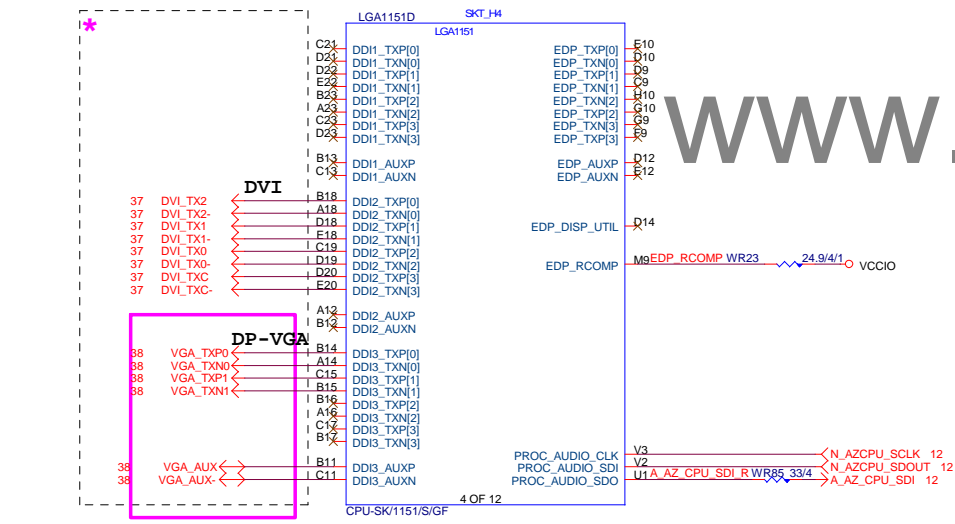
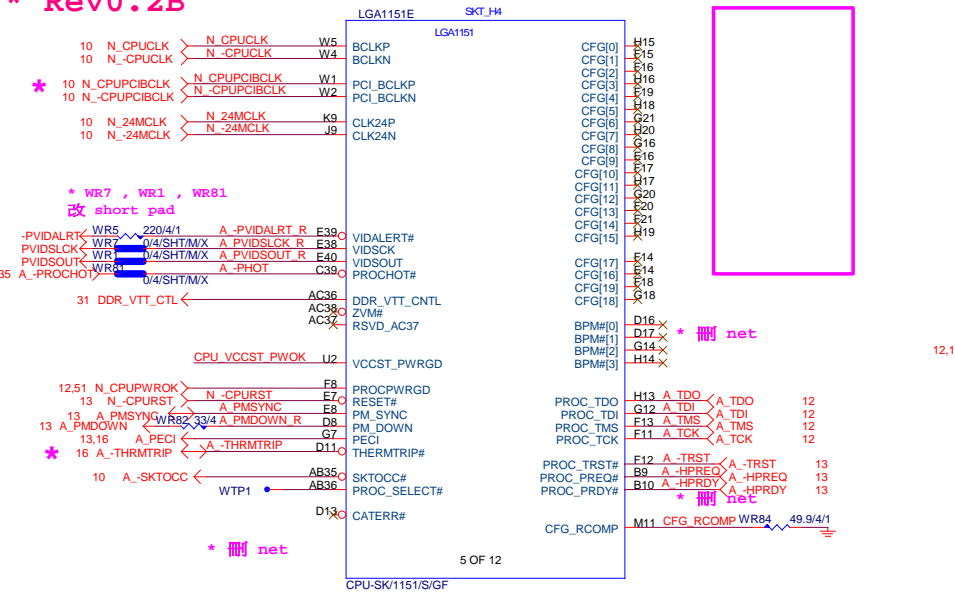
Component value change history

[illegible][illegible]

BLOCK DIAGRAM



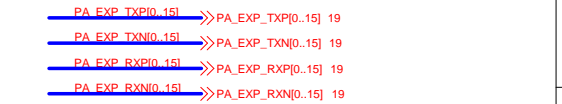
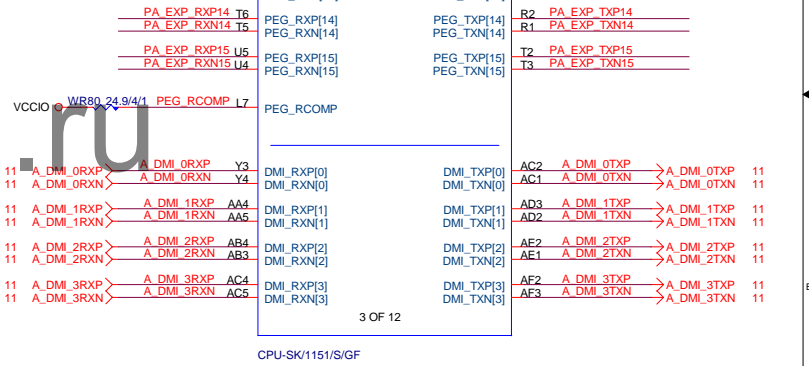
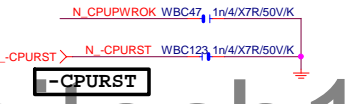
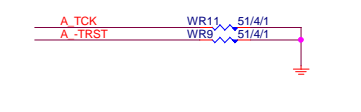
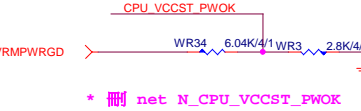
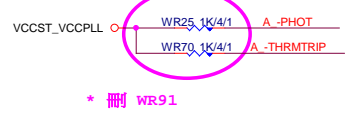
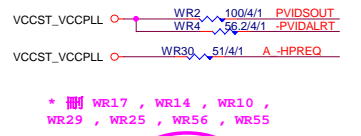
* Rev0.2B



G-15u : (CPU-SK/1151/S/15)
10SC1-F01151-11R / 10SC1-F01151-12R
G-FL : (CPU-SK/1151/S/GF)
10SC1-F01151-21R / 10SC1-F01151-22R

4 layer HDMI/DP/eDP/=====4/4/4//15
6 layer HDMI/DP/eDP/=====4/5.5/4//15

Impedance=85 +- 15%



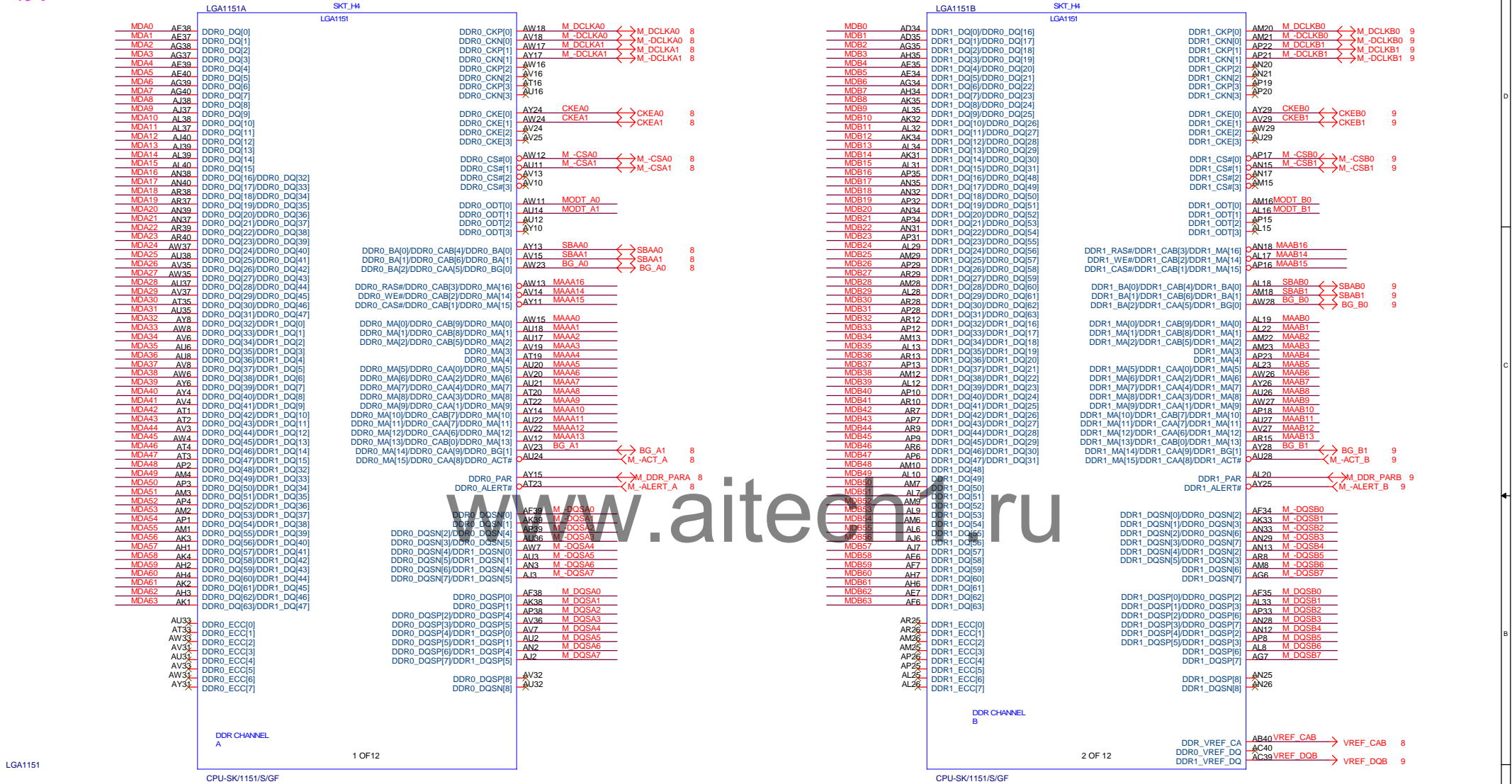
4 layer PEG/DMI=====4/4/4//15
6 layer PEG/DMI=====4/5.5/4//15
Impedance=85 +- 15%

CFG[2]:x16 Lane Numbering
Reversal. 1=
NORMAL;0=reversal
CFG[4]: eDP
enable:1:disable/0=enable
CFG[6:5]:PCI Express* Bifurcation; 1l=
1 x16 PCI Express;10=2x8 PCI Express
CFG[7]: PEG Training;1=(default) PEG Train
immediately following RESET#;0=PEG Wait
for BIOS

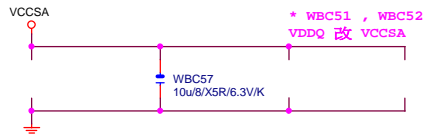
Bifurcation Config.	Signals Lanes		
	CFG[6]	CFG[5]	CFG[2]
1x16	1	1	1
1x16 Reversed	1	1	0
2x8	1	0	1
2x8 Reversed	1	0	0
1x8+2x4	0	0	1
1x8+2x4 Reversed	0	0	0

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CPU LGA1151-A		
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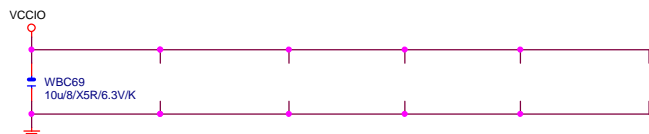
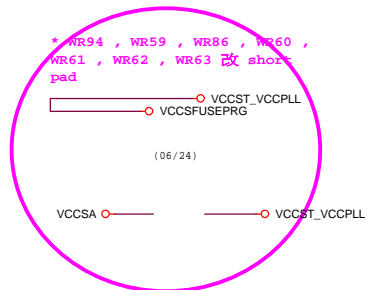
* 改DDR4 net



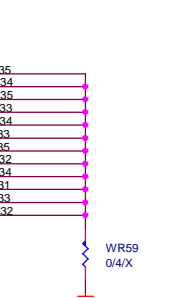
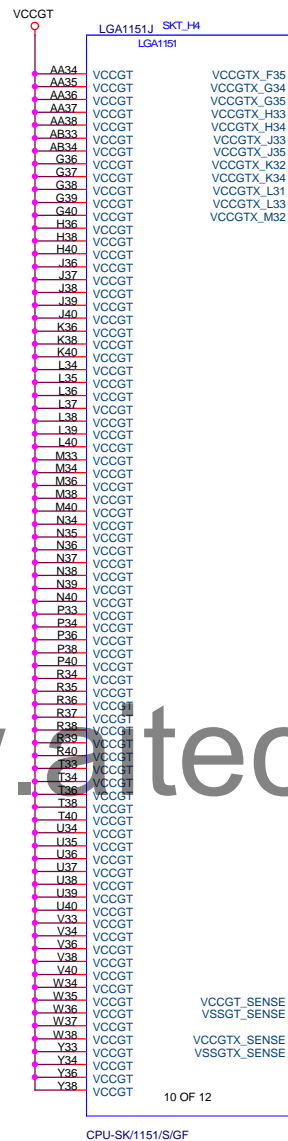
* 刪 WBC50 電容



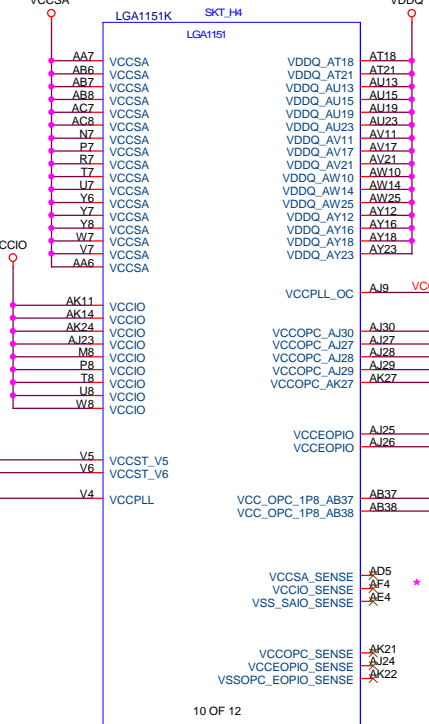
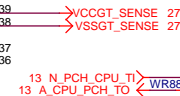
* 刪 WBC124 , WBC125 , WBC126 , WBC127 電容



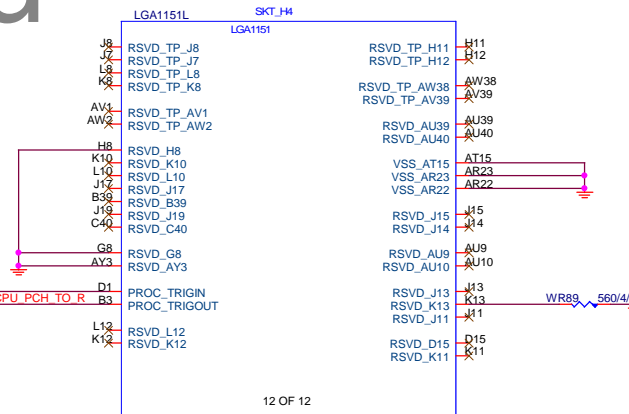
* 刪 VCCGT 電容



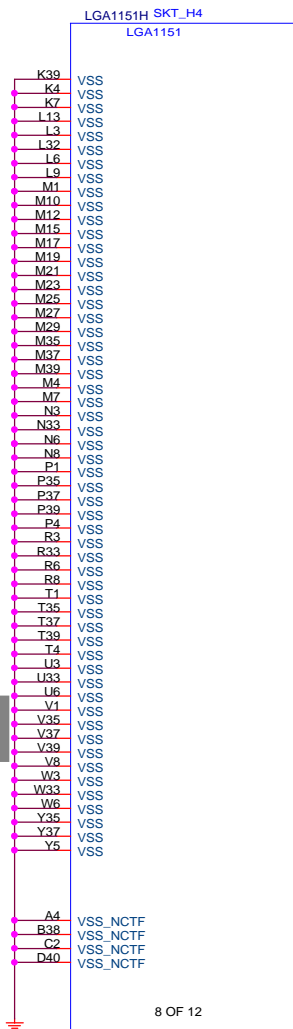
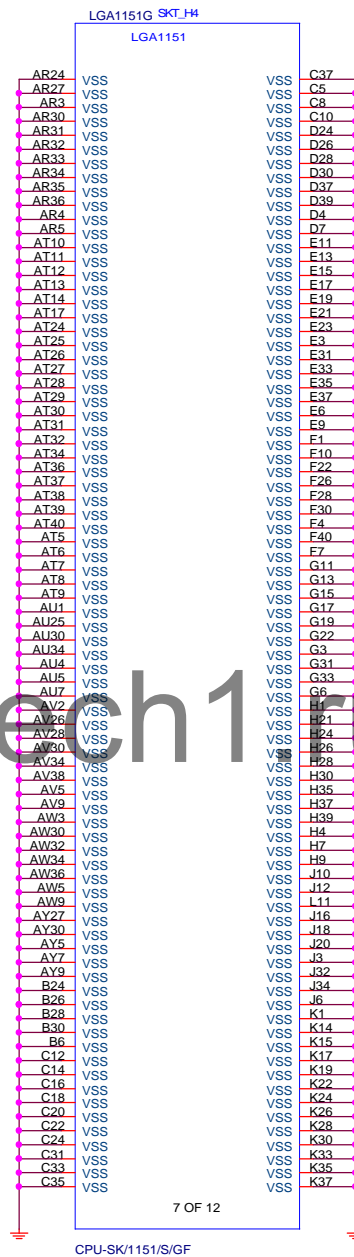
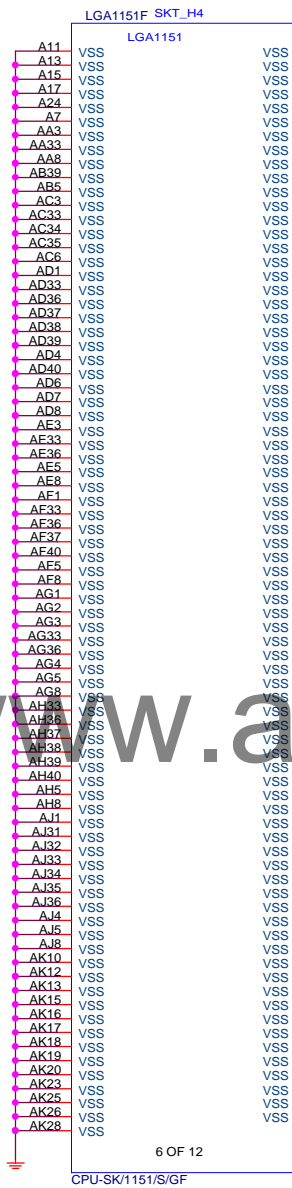
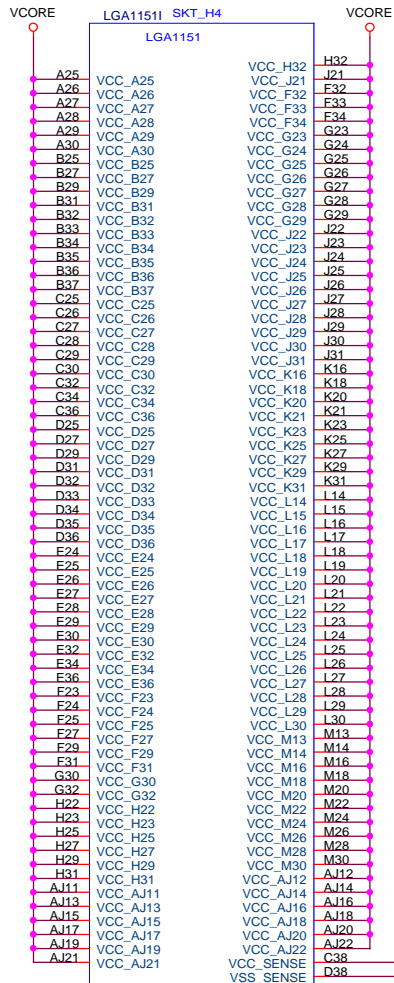
VCCST_VCCPLL
VCCSFUSEPRG
VCCST_VCCPLL



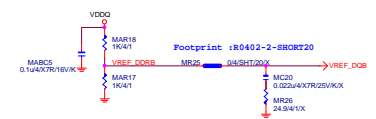
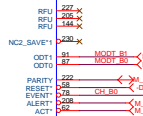
CPU-SK/1151/S/GF



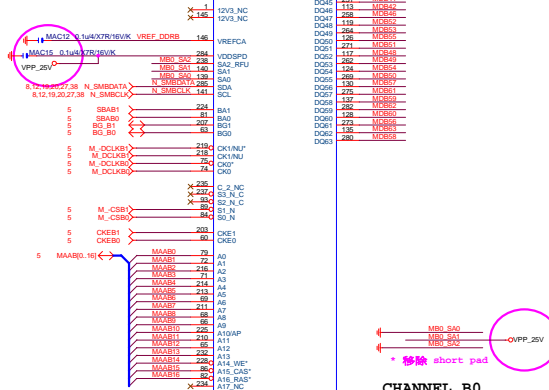
CPU-SK/1151/S/GF



* 刪 Vcore 電容

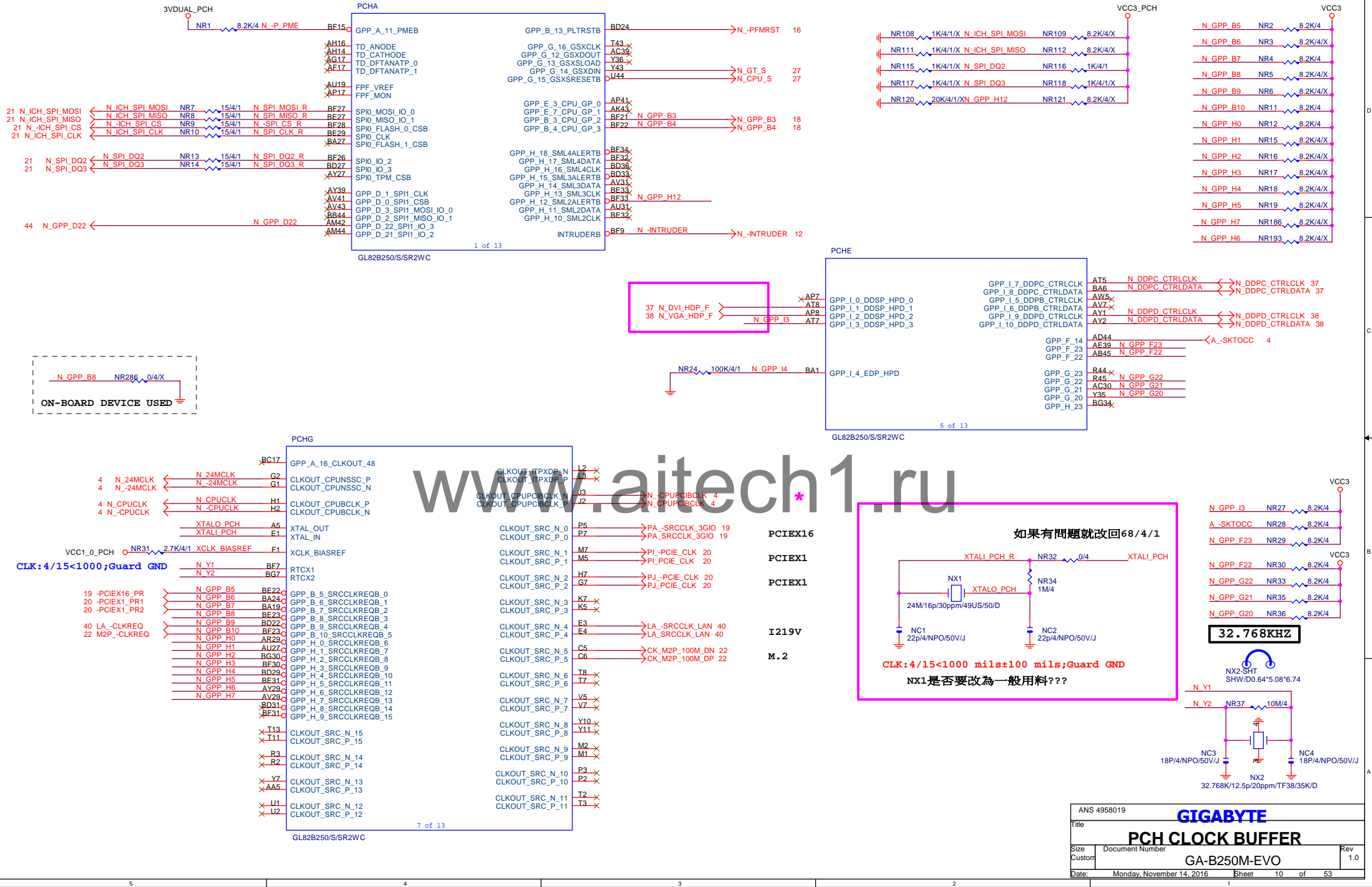


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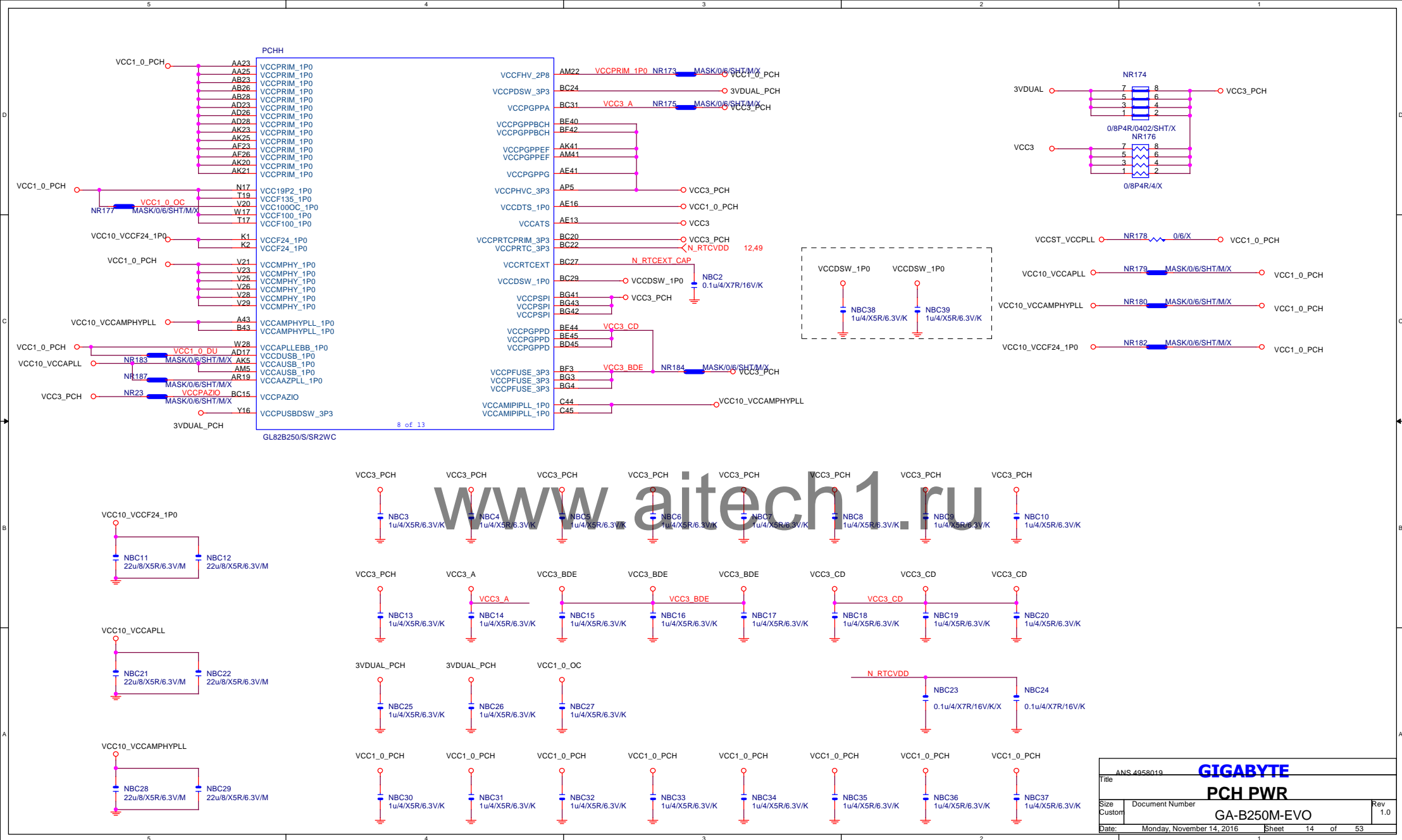


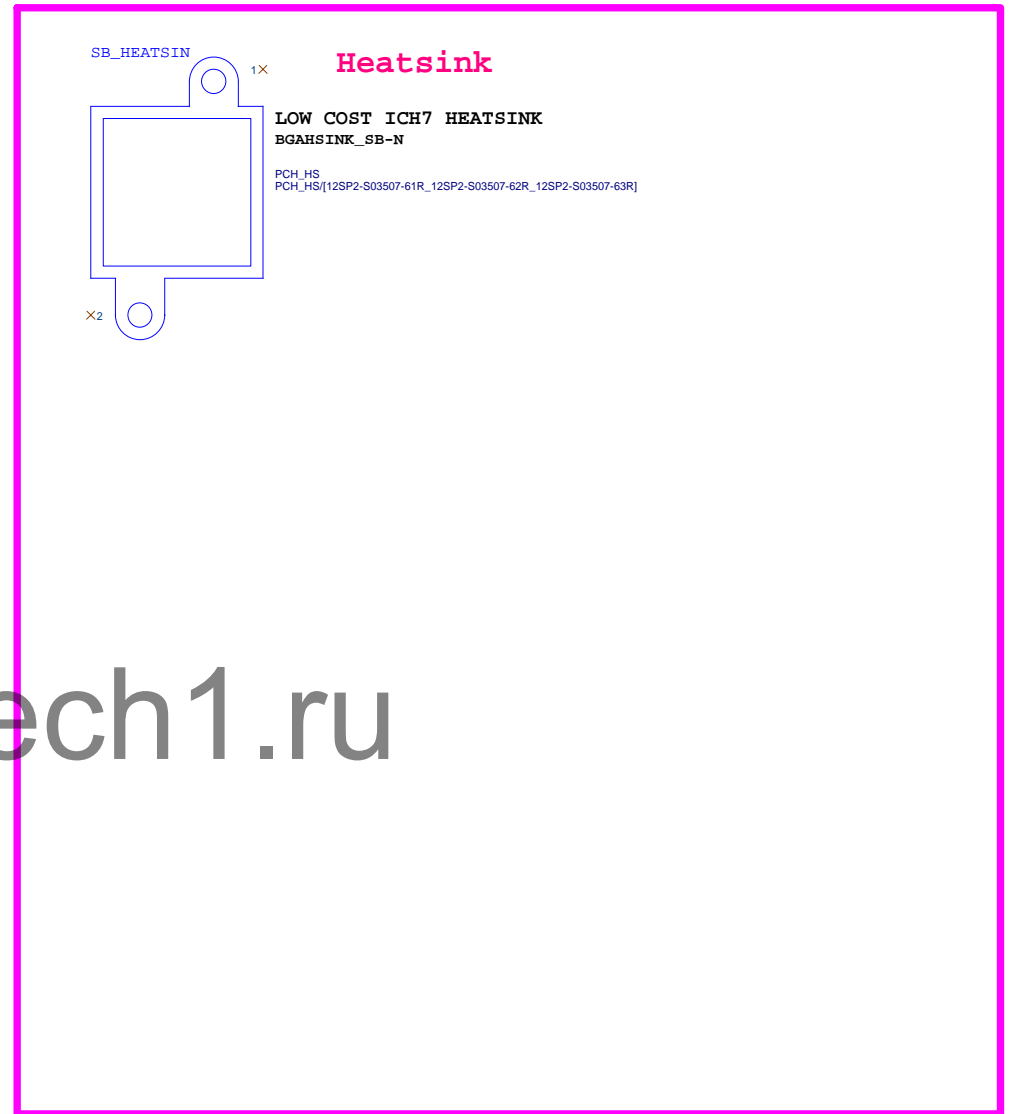
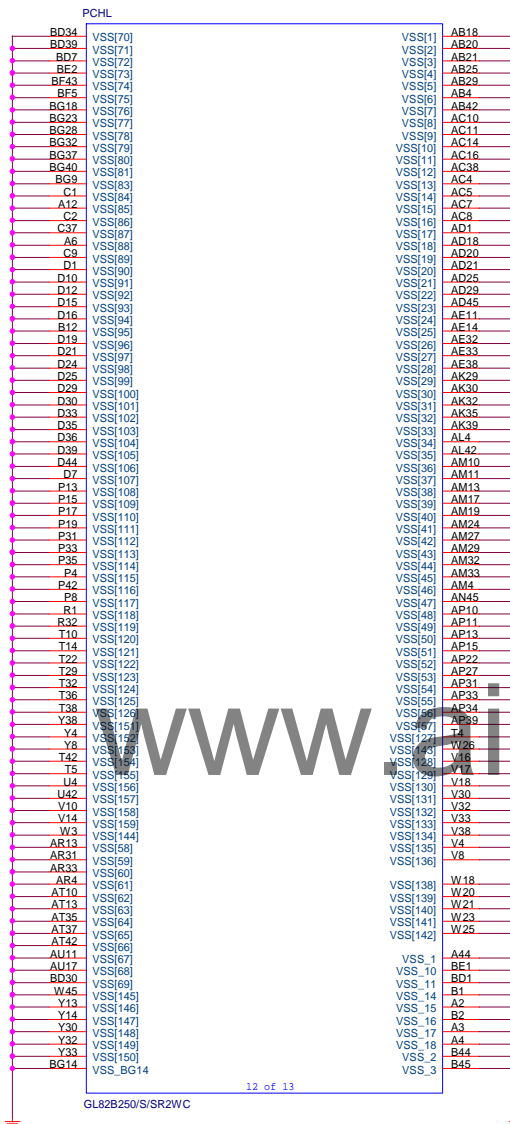
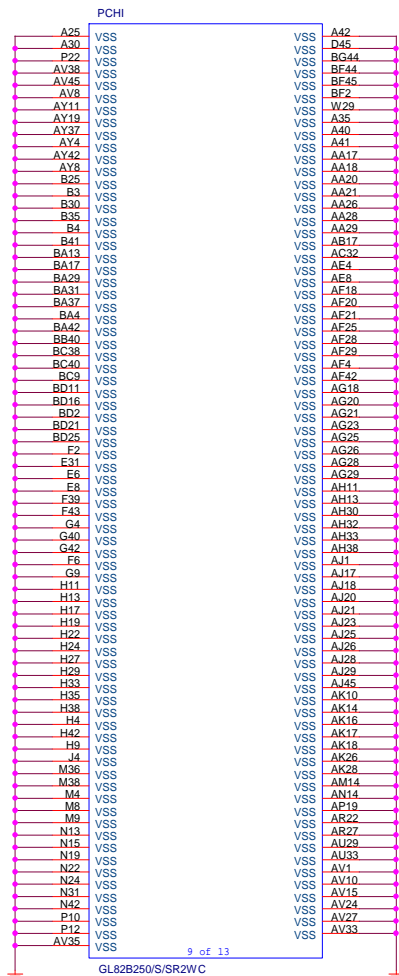
CHANNEL B0
SA2:0=010

DDR4/288/PYAD/GF/TWO LATCH

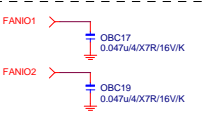
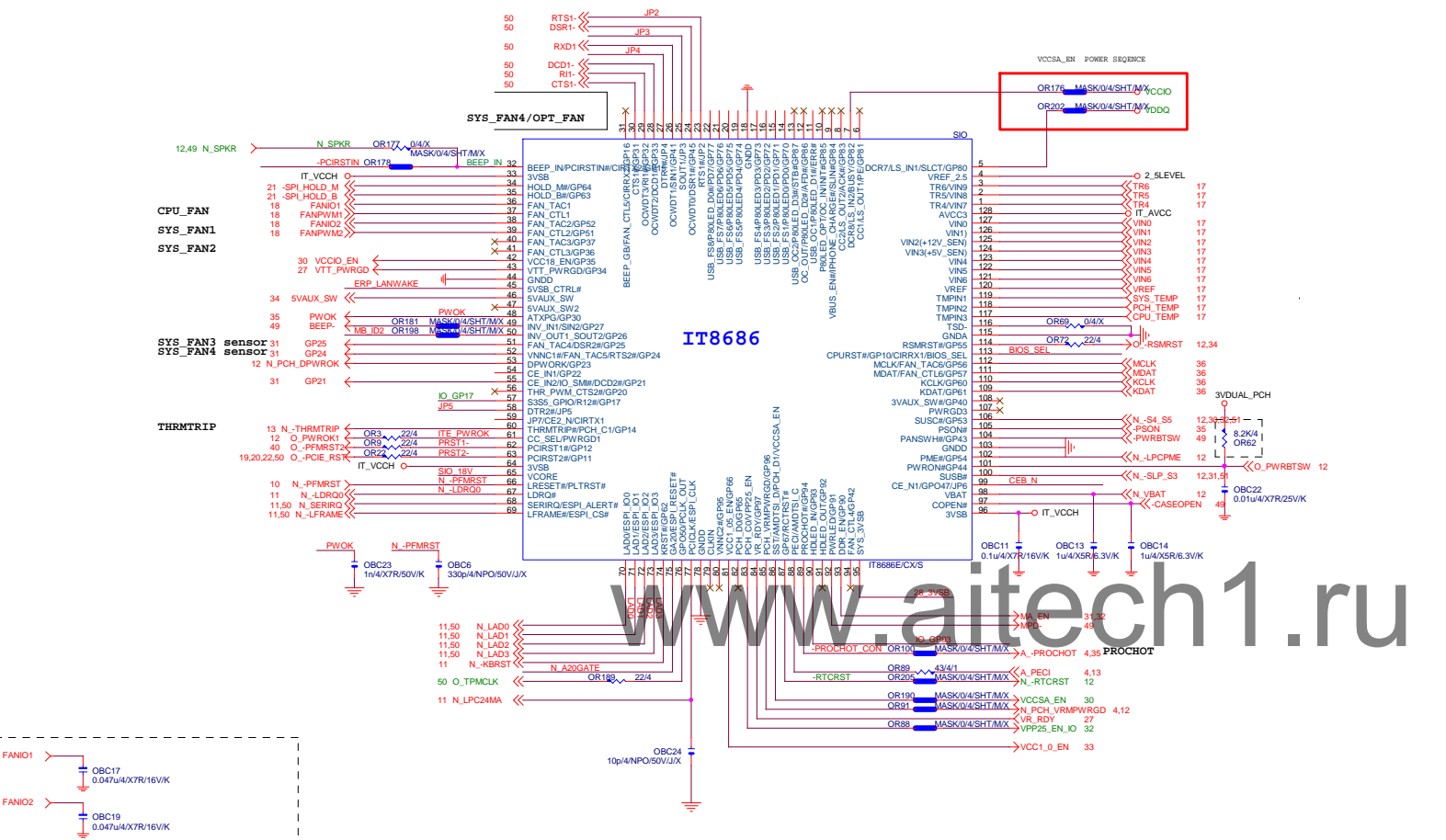








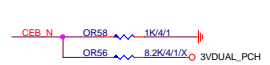
ANS 4958019		GIGABYTE	
Title		PCH GND	
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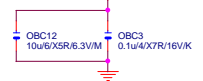
Single bios Del

FAN TABLE	
CPU_FAN	FAN_CTL1 FAN_TAC1
SYS_FAN1	FAN_CTL2 FAN_TAC2
SYS_FAN2	FAN_CTL3 FAN_TAC3
SYS_FAN3	FAN_CTL4 FAN_TAC4
SYS_FAN4	FAN_CTL5 FAN_TAC5
THRMTRIP	PIN56
PROCHOT	PIN89

DUAL BIOS OPT STRAP



SIO CAP



Placement CPU

4 A-THRMTRIP < WR110 1K/4/1 N-THRMTRIP
CPU 端 A-THRMTRIP 不可與 PCH 及 SIO
N-THRMTRIP 直接連接。
否則會出現無法拉 Low 情況。

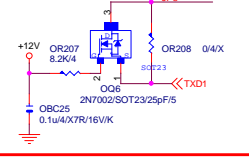
SIO 18V

internal power pin, max 22nF cap

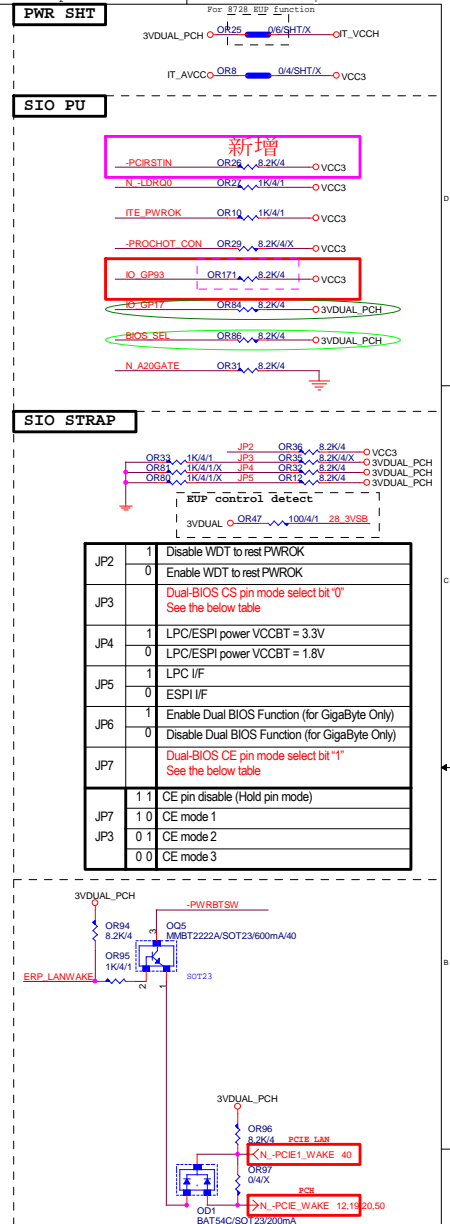
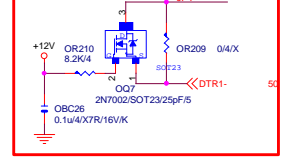


CLOSE SIO PIN4 2.5LEVEL

for LPC/eSPI power mode

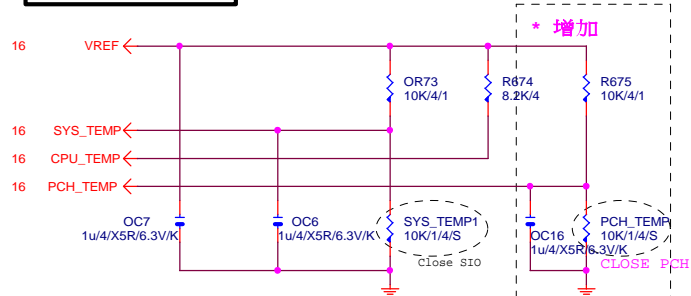


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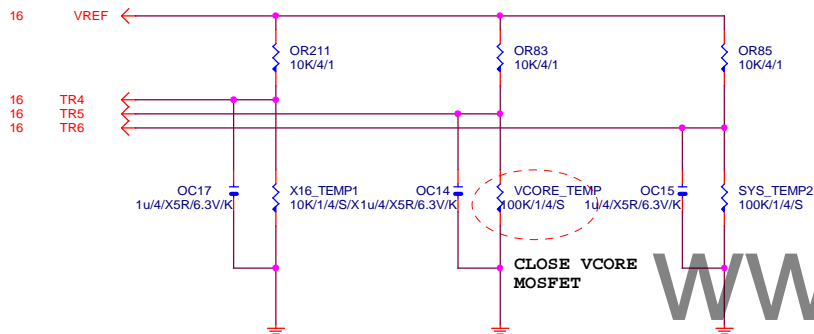


JP2	1	Disable WDT to rest PWRK
JP2	0	Enable WDT to rest PWRK
JP3	1	Dual-BIOS CS pin mode select bit "0"
JP3	0	See the below table
JP4	1	LPC/ESPI power VCCBT = 3.3V
JP4	0	LPC/ESPI power VCCBT = 1.8V
JP5	1	LPC I/F
JP5	0	ESPI I/F
JP6	1	Enable Dual BIOS Function (for GigaByte Only)
JP6	0	Disable Dual BIOS Function (for GigaByte Only)
JP7	1	Dual-BIOS CE pin mode select bit "1"
JP7	0	See the below table
JP7	1 1	CE pin disable (Hold pin mode)
JP7	1 0	CE mode 1
JP3	0 1	CE mode 2
JP3	0 0	CE mode 3

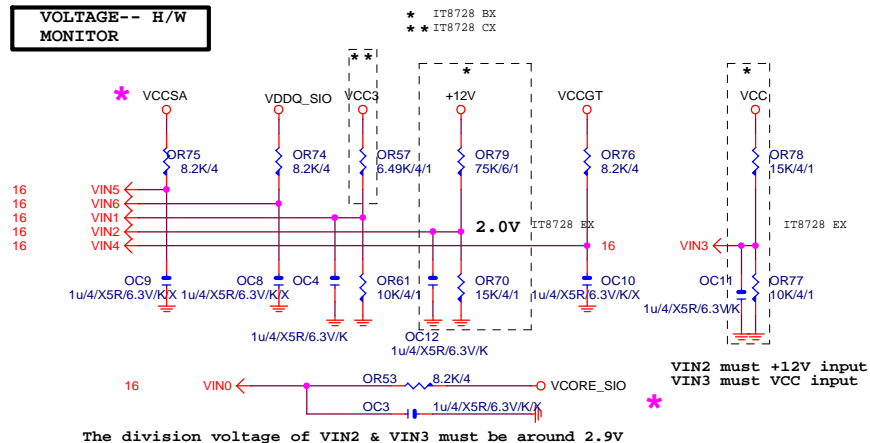
TEMP H/W MONITOR



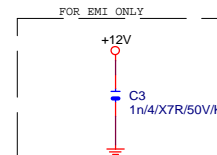
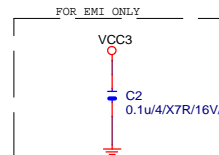
5個FAN時使用



VOLTAGE-- H/W MONITOR



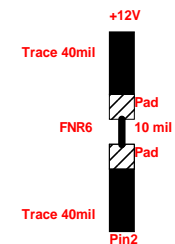
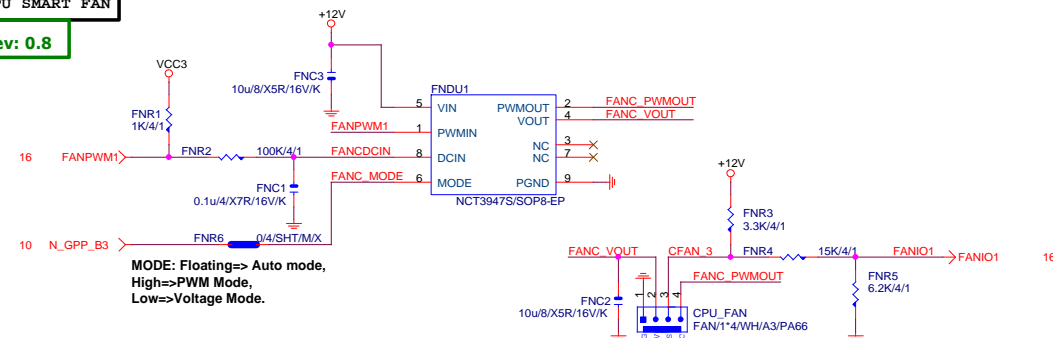
★Update 2015-04.24



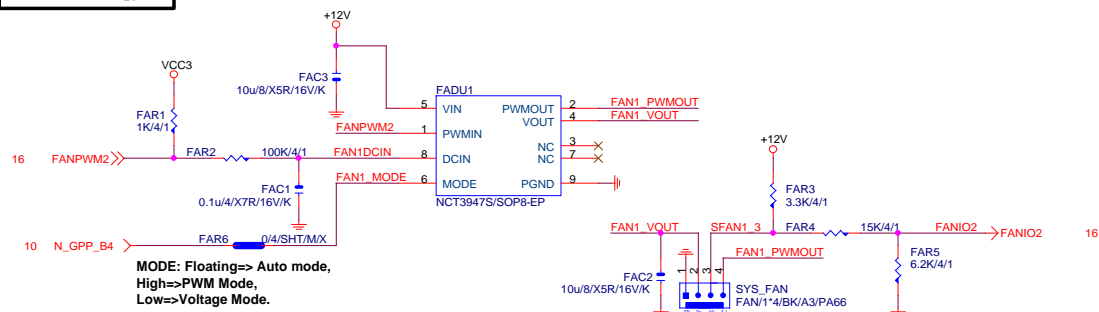
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Title			HWM,KB/MS, FAN CTRL
Size	Document Number	Rev	
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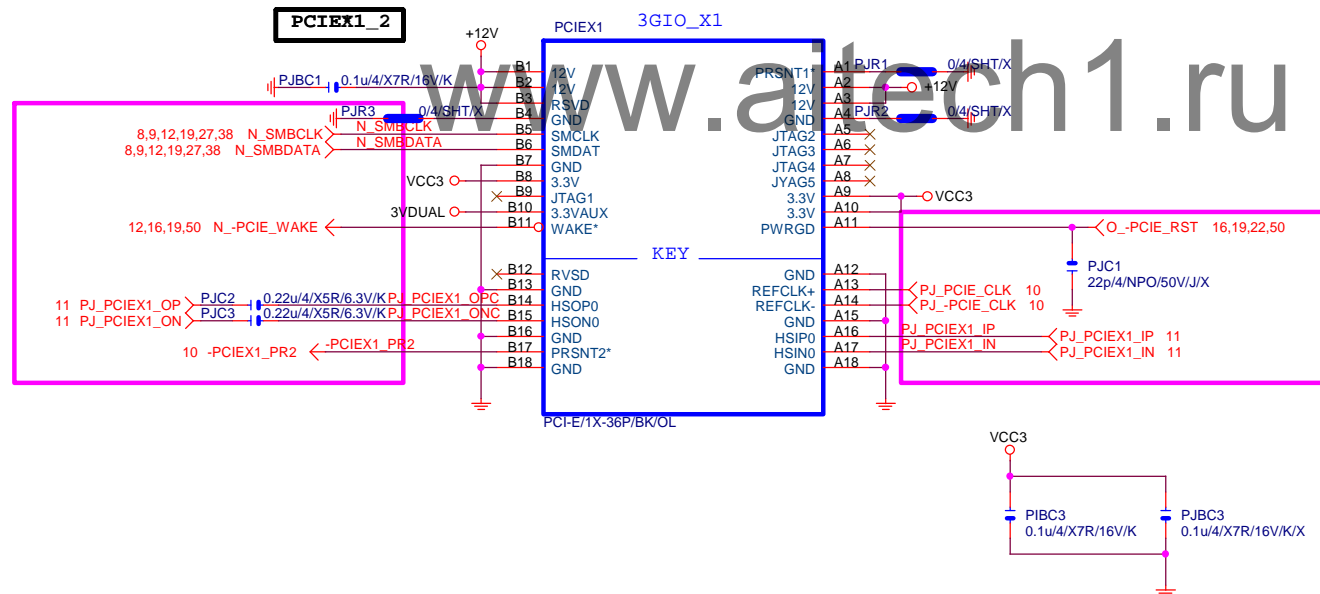
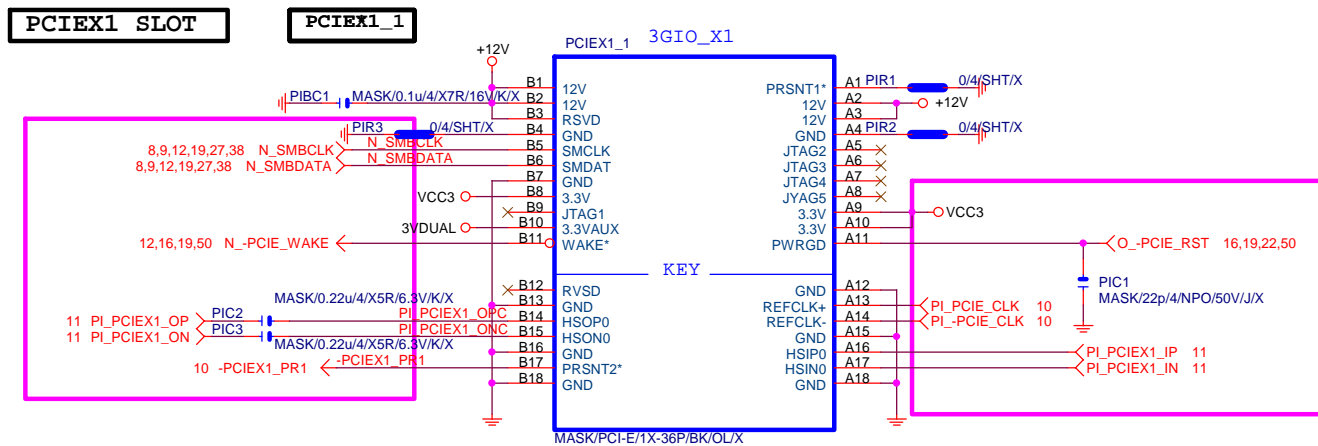


```
A. SYSTEM FAN1
```



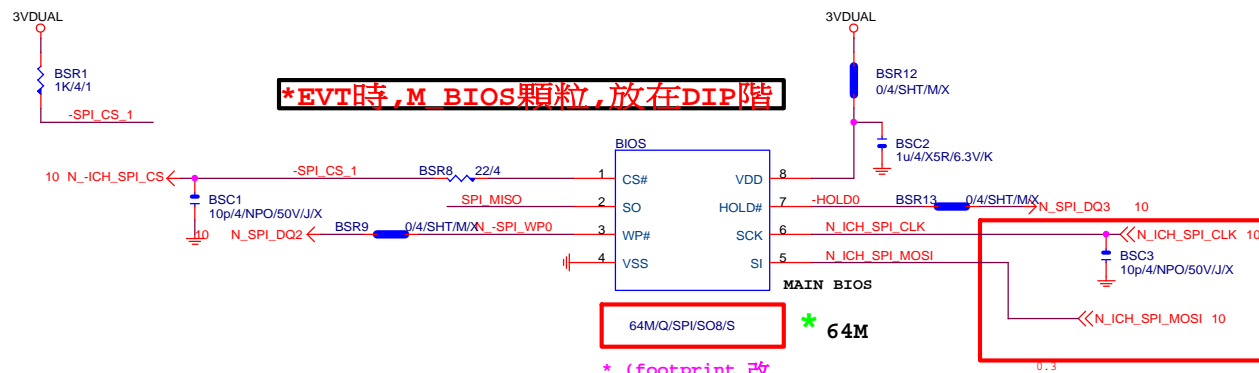
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Title				
FAN CTRL				
Size	Document Number			Rev
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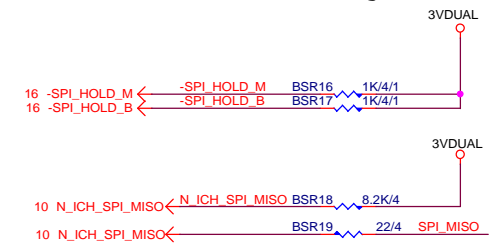
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Title		
PCIE_X4		
Size	Document Number	Rev
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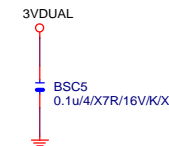
* (footprint 改
SOIC8-SPI-SOCKET)
* (MP footprint 改 IC8-BIOS)

MOSI For DMI RX Termination Voltage

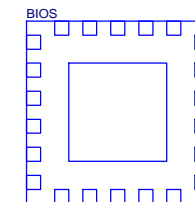


BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating
0 means PD 1K



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LCP/G-FL/1.27mm/200MIL/WHITE[10SL2-000008-31R]/X

* 試產先上, PVT 移除

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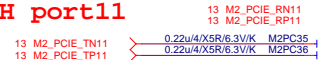
Title			BIOS
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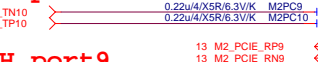
M.2 Lane4 from PCH port12



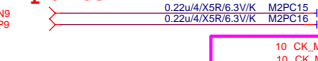
M.2 Lane3 from PCH port11



M.2 Lane2 from PCH port10

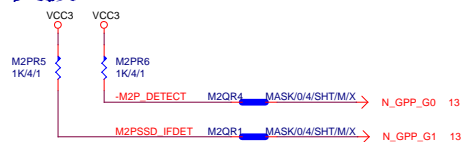


M.2 Lane1 from PCH port9



需與M2_-CLKREQ對應

支援SATA and M.2 function



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M.2 X4			
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Title			
Gigabyte Technology			
M.2X4_ S4~S5 SWITCH			
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Title			
IT8892E			
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GIGABYTE™			
Title PCI SLOT 1&2			
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Rev 0.1

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Title

ASM1085 POWER

Size
Custom

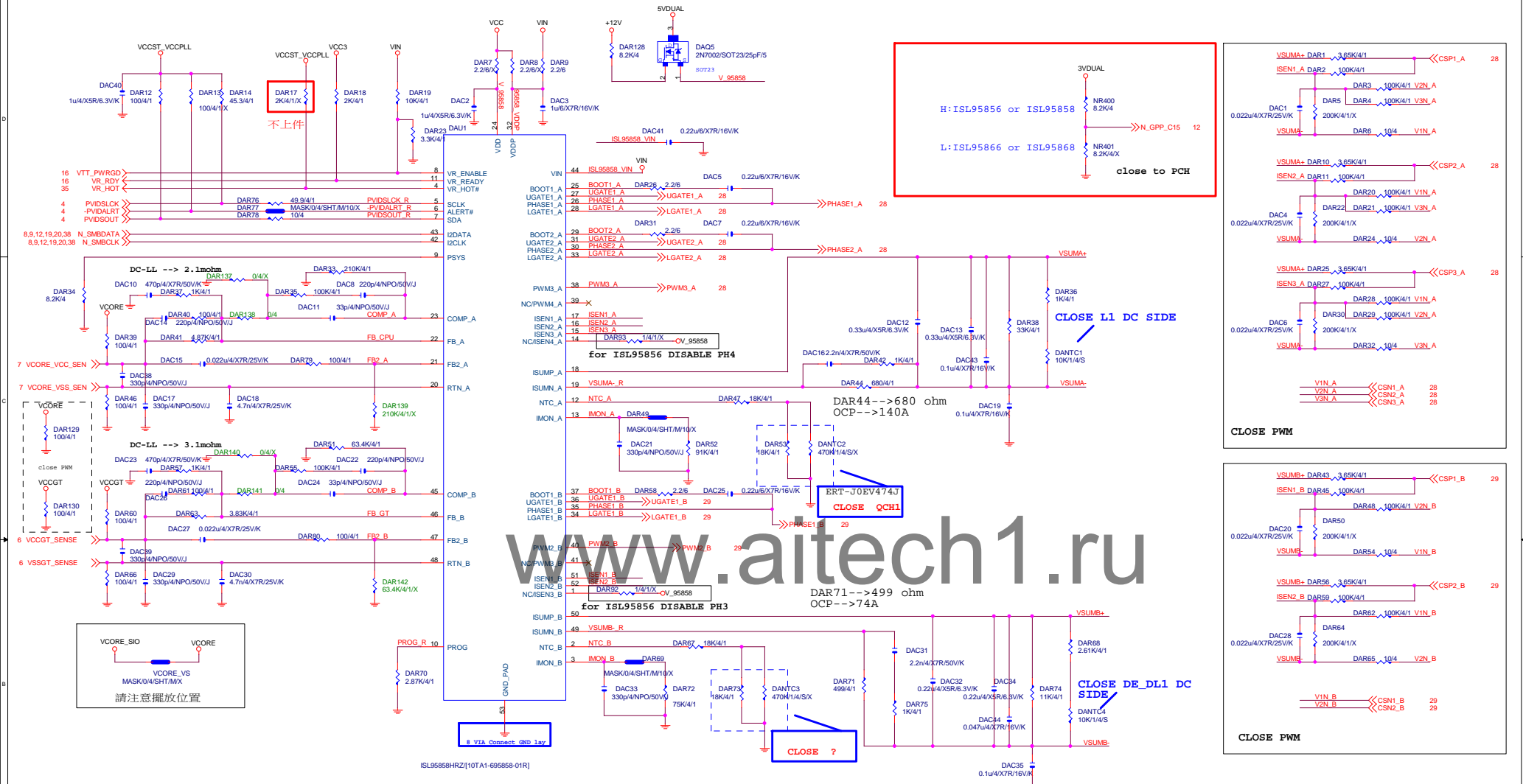
Document Number

GA-B250M-EVO

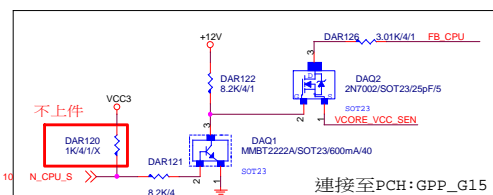
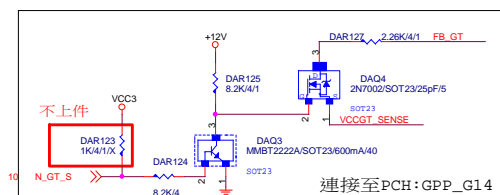
Rev
1.0

Date: Monday, November 14, 2016

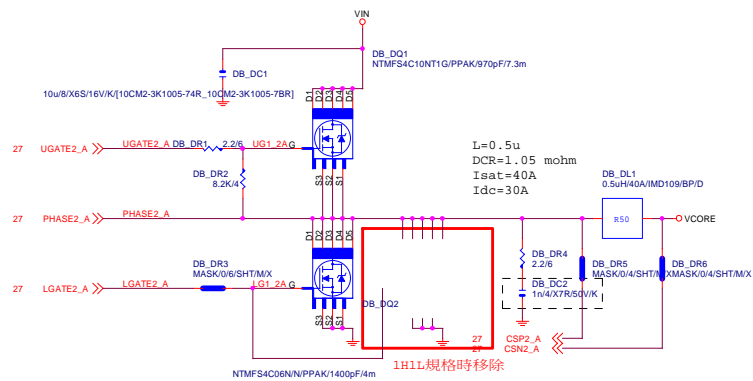
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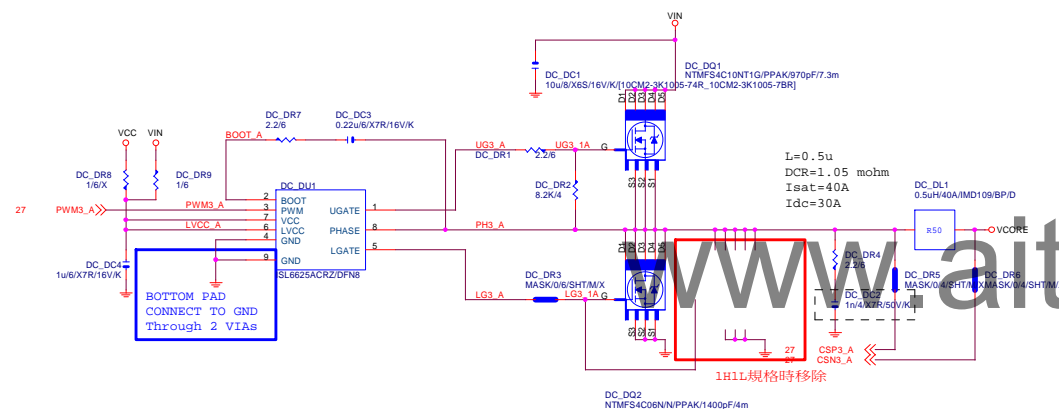
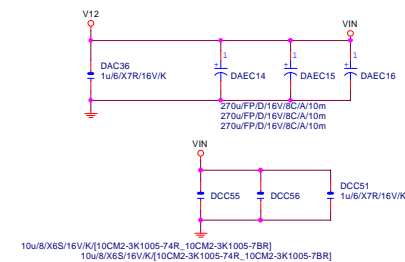
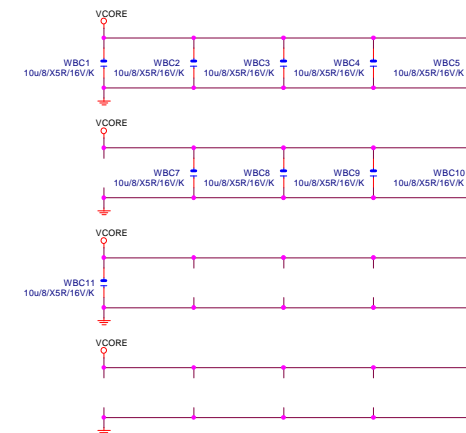
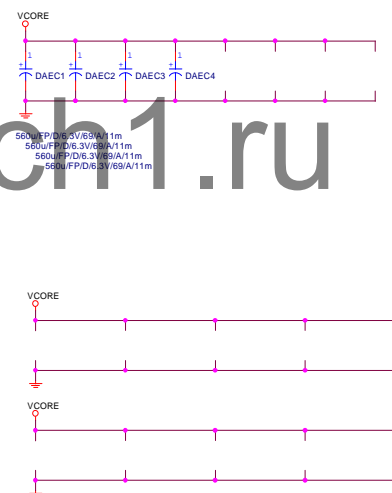
VCOLE	ISL95858	ISL95868	VCCGT	ISL95858	ISL95868
DAR137	X	V	DAR140	X	V
DAR138	V	X	DAR141	V	X
DAR139	X	V	DAR142	X	V
DAC15	V	X	DAC27	V	X
DAR79	V	X	DAR80	V	X
DAR33	V	X	DAR51	V	X



VCORE



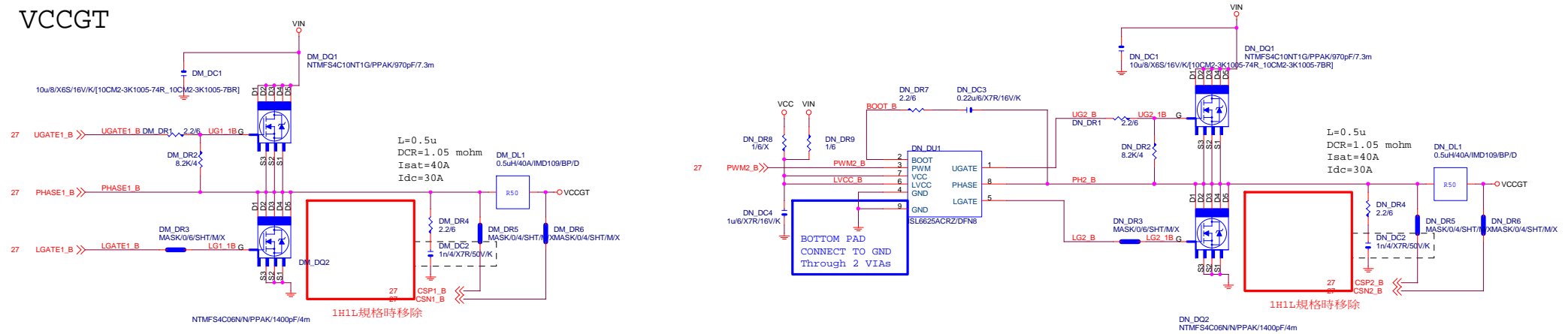
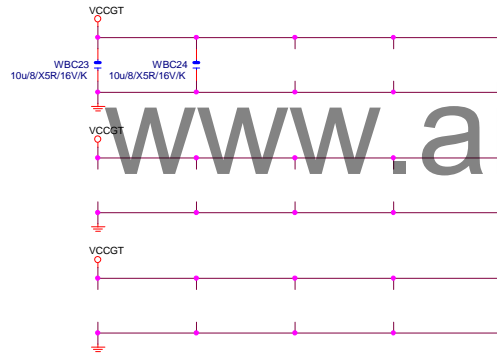
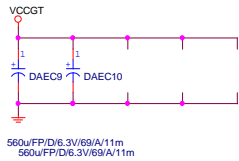
VIN CAP 270u*3PCS

VCORE CAP 560u*4PCS
10u*10PCS

GIGABYTE™

Title			
ISL95868_MOS			
Size	Document Number	Rev	
Custom	GA-B250M-EVO	1.0	
Date:	Monday, November 14, 2016	Sheet	28 of 53

VCCGT

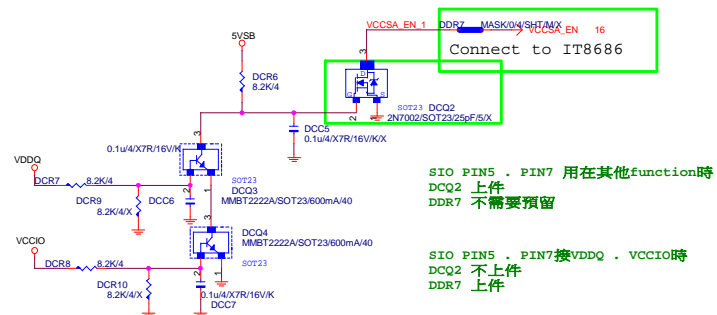
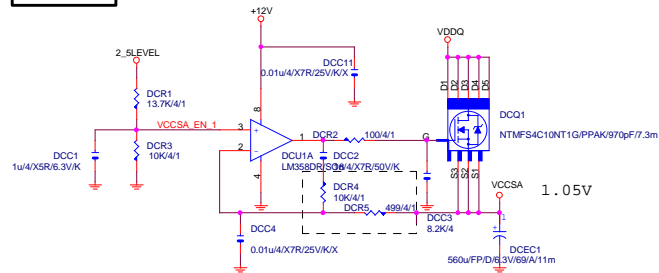
VCCGT CAP 560u*2PCS
10u*2PCS

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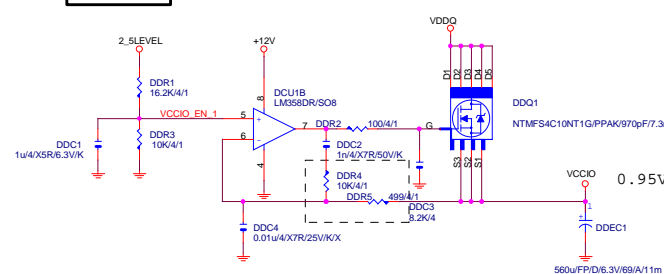
GIGABYTE™

Title		
ISL95868_MOS		
Size	Document Number	Rev
Custom	GA-B250M-EVO	1.0
Date:	Monday, November 14, 2016	Sheet 29 of 53

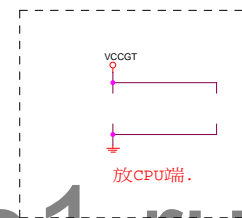
VCCSA



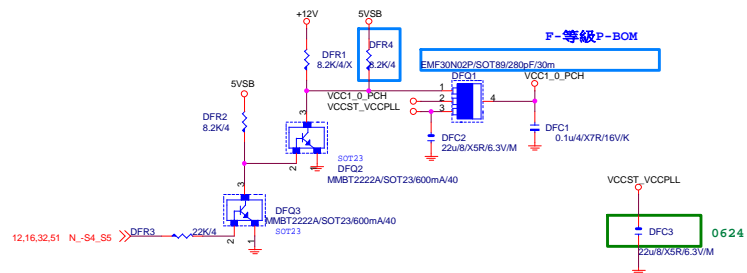
VCCIO



VCCIO_EN 1 DDR8 MASK0/4/SHTAMX
Connect to IT8686



VCCST_VCCPLL



REV:0.2

DDR4

CHOKE與CAP料號可變

DDR VIN CAP
560u*2PCS

SUPPORT DDR4 1.2V

25A MAX

L=1u
DCR=2.5 mOhm
Isat=35A
Idc=28A請放置CHOKE--出來位置,先預留.
請自行確認ripple後再決定是否上件

Remote sense請從最重的負載端點拉回

MA_DR38.MA_DC15

VPP_25V使用8120.8068A.RT8237時上件

PWR SEQ

CLOSE TO DDR POWER PLANE

For power sequence require

VPP_25V使用8120時上件

MAU1上RT9045時上件(不可MASK)
MA VTT REF

DDRVTT

DDRVTT CAP

4 DDR_VTT_CTL MAR110 MASK/0/4/SHT/M/10/X_DDRVTT_EN
2,16,51 N_SLP_S3 MAR111 MASK/0/4/SHT/M/10/0_DDRVTT_BOOT

GIGABYTE™

RT8237_DDR4 POWER

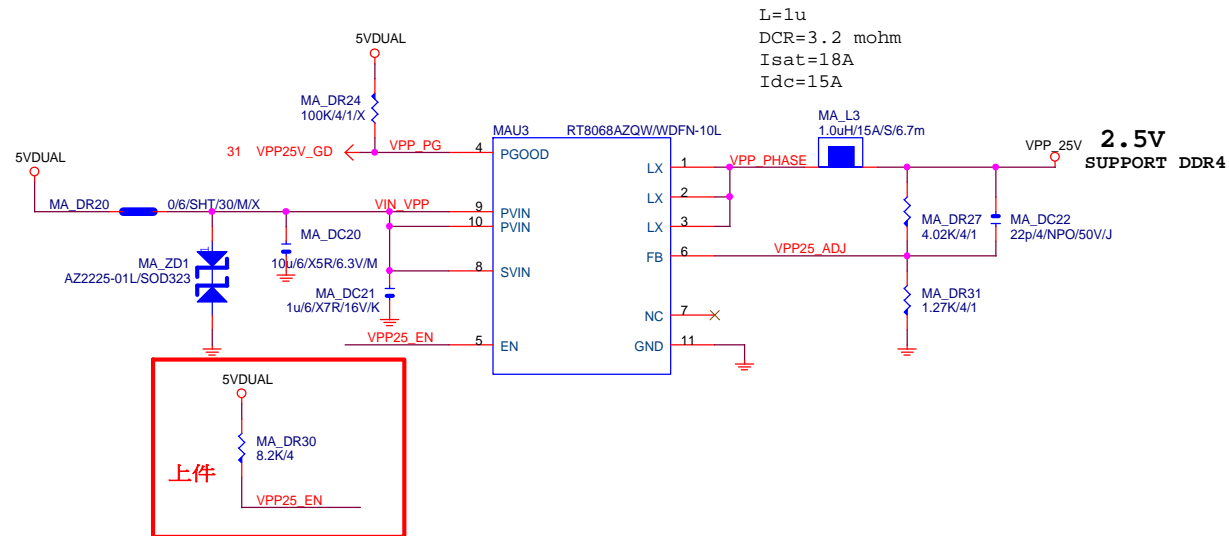
Size	Document Number	Rev
Custom	GA-B250M-EVO	1.0

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REV:0.1

VPP_25V

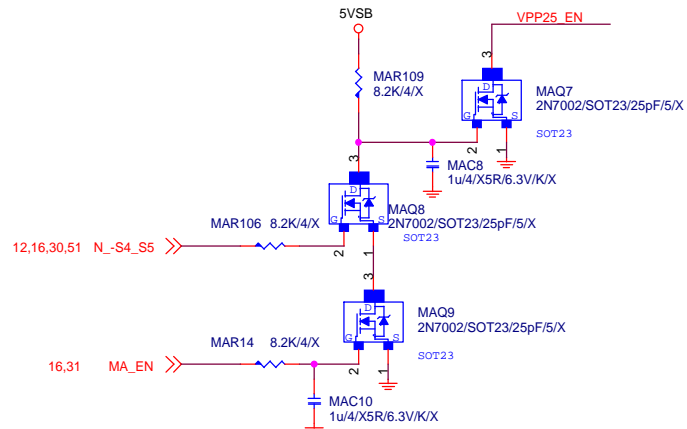
CHOKE與CAP料號可變



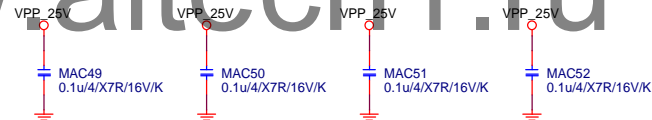
www.aitech1.ru

PWR_SEQ

* 刪 MA_DR32

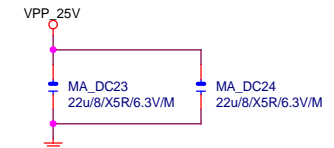


16 VPP25_EN_IO MAR114 MASK/0/4/SHT/M/X VPP25_EN



VPP CAP 22u*1PCS

* 大電容 x0

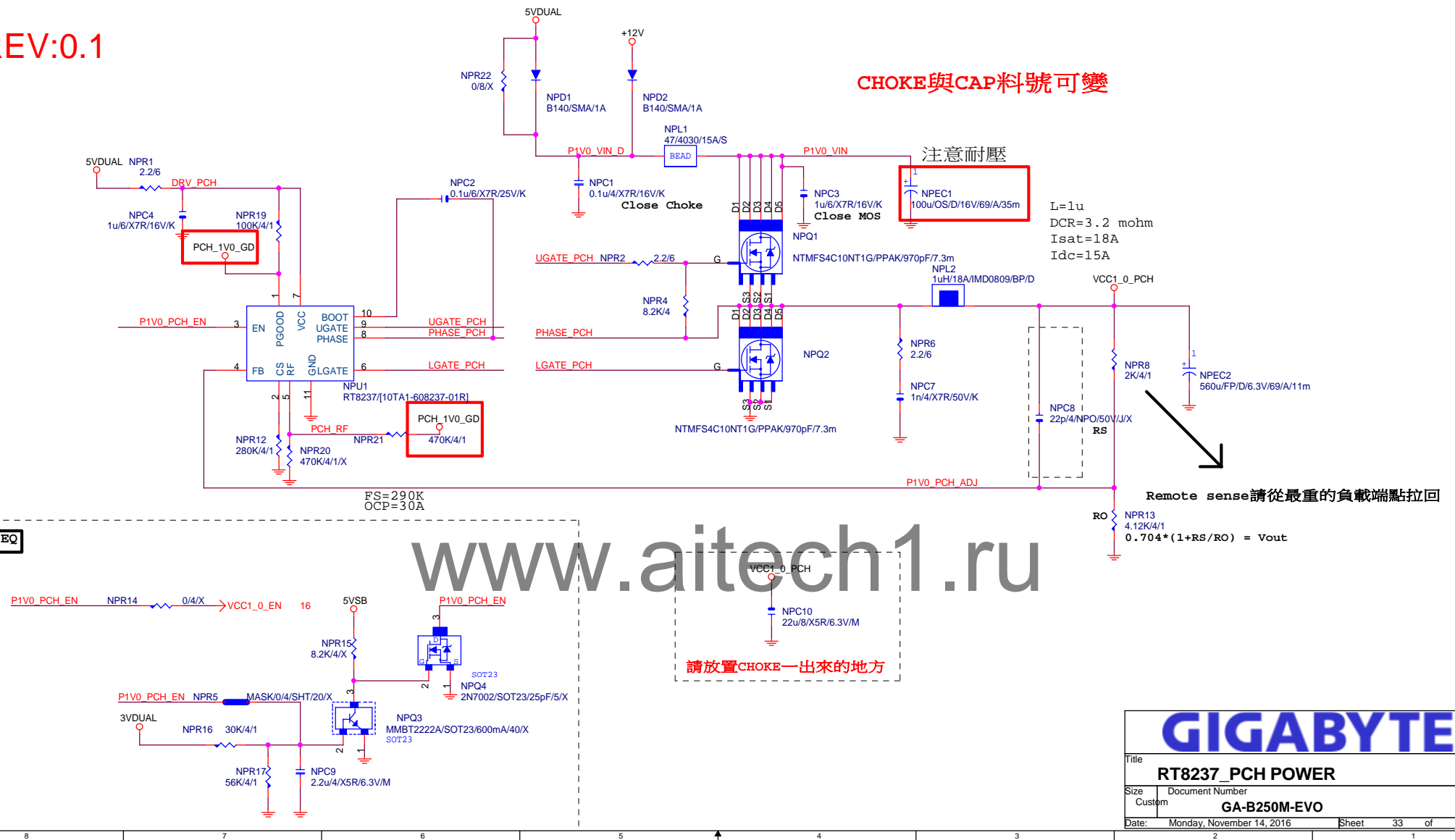


GIGABYTE™

Title			
RT8068A_VPP25 POWER			
Size	Document Number		Rev
Custom	GA-B250M-EVO		1.0
Date:	Monday, November 14, 2016	Sheet 32 of	53

REV:0.1

CHOKE與CAP料號可變



GIGABYTE™

Title			
RT8237_PCH POWER			
Size	Document Number	Rev	
Custom	GA-B250M-EVO	1.0	
Date:	Monday, November 14, 2016	Sheet	33 of 53

REV: 0.51

5VDUAL

REV:0.51

* 5Vdual , update
from SKL 0.2B

[illegible]

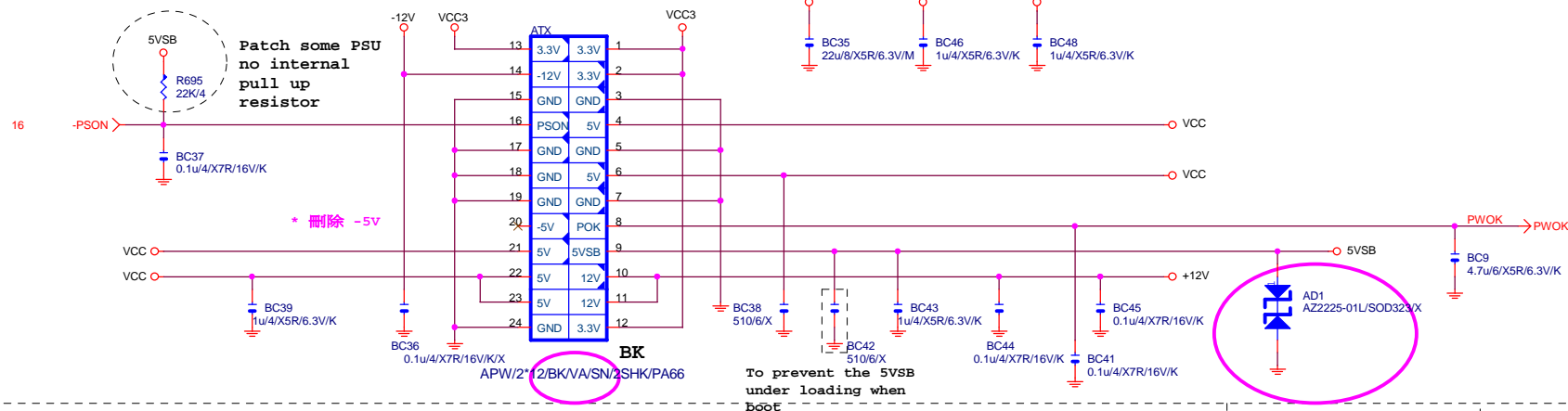
The schematic shows the NQ9 L1117LG/N/SOT223/1A voltage regulator. The input is connected to 5VSB. The output is connected to 3VDUAL_PCHO. The circuit includes two resistors, NR217 (301/4/1) and NR218 (510/4/1), and two capacitors, NBC67 (0.1u/4/X7R/16V/K) and NBC68 (1u/4/X5R/6.3V/K).

[illegible]

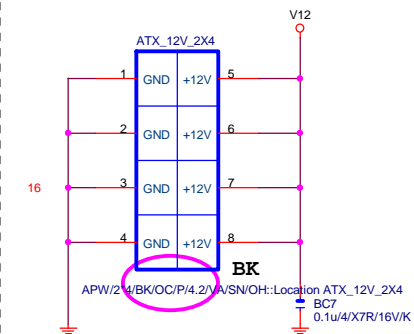
Gigabyte Technology

Title			
DISCRETE POWER			
Size	Document Number		Rev
Custom	GA-B250M-EVO		1.0
Date:	Monday, November 14, 2016	Sheet	34 of 53

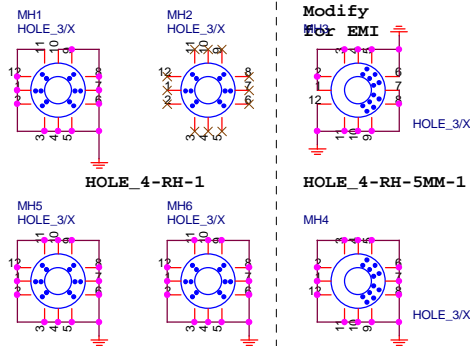
ATXX24 POWER CONNECTOR



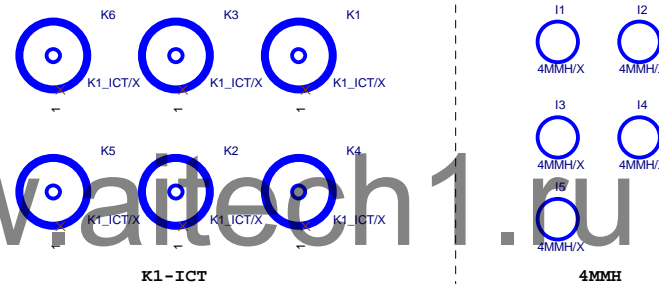
ATXX4 POWER CONNECTOR



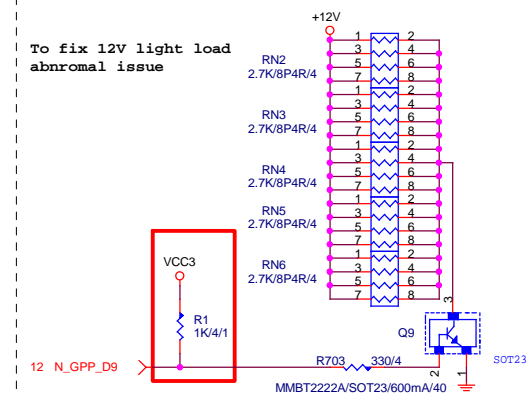
螺絲孔



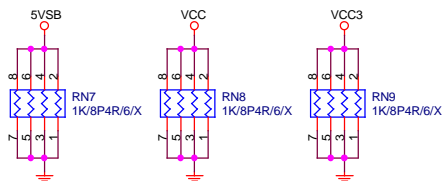
固定孔/光學點



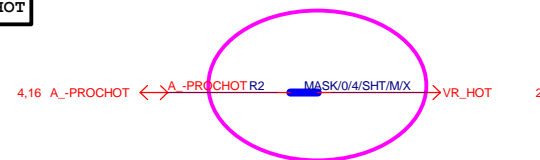
+12V DUMMY LOAD



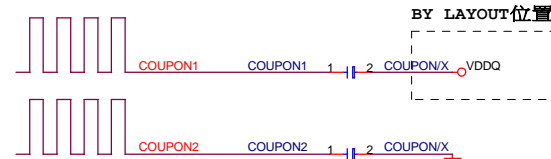
DUMMY LOAD



-PROHOT



COUPON



Gigabyte Technology

Title			
ATX POWER CONNECTOR			
Size Custom	Document Number	GA-B250M-EVO	Rev 1.0
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Rev: 81

KB/MS/6P/PC99/OS/RA/D/2

MSDATA 7

MSCLK 8

KBDATA 1

KBCLK 2

KB 3

KB MS

MS

KB

10

9

4

3

FSVCC_KM

KMBC1
0.1u/4/X7R/16V/K

\$0.118

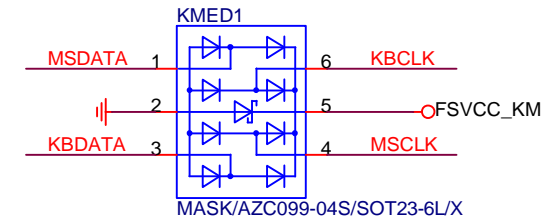


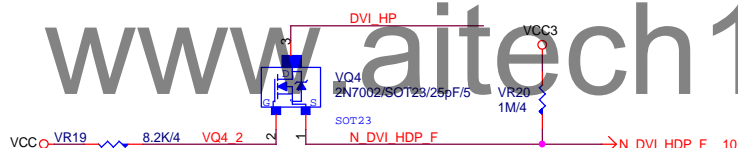
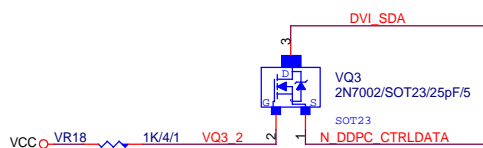
Figure 10: KMR1~KMR4 pin connections. The diagram shows the pin connections for KMR1, KMR2, KMR3, and KMR4. KMR1 is connected to KDAT (82/6), KMR2 to KCLK (82/6), KMR3 to MSDATA (82/6), and KMR4 to MSCLK (82/6). The KMR1 pin is also connected to FSVCC_KM. The KMR1 pin is also connected to KMC1, KMC2, KMC3, and KMC4, which are connected to ground via 180pF capacitors. The KMR1 pin is also connected to KMC1, KMC2, KMC3, and KMC4, which are connected to ground via 180pF capacitors.

USB OC PROTECT

Title					KB_MS_USB				
Size A	Document Number							Rev	
	GA-B250M-EVO							1.0	
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Rev: 0.8

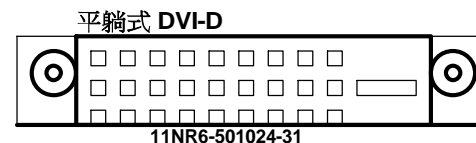
NET 可變



DVI CONN

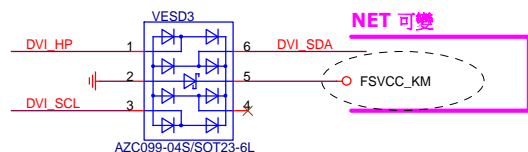


* ECVCC, K

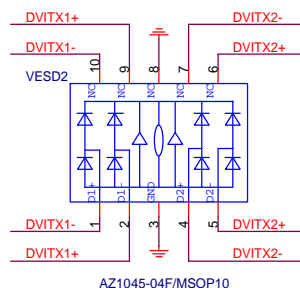


NET 可變

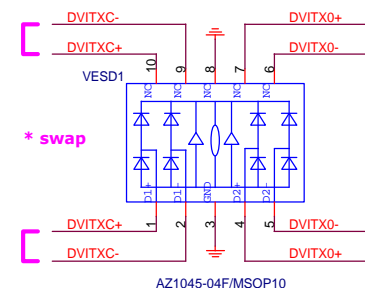
FSVCC_KM



Close to connector



Close to connector



Close to connector

Gigabyte Technology

Title

DVI

Size	Custom
------	--------

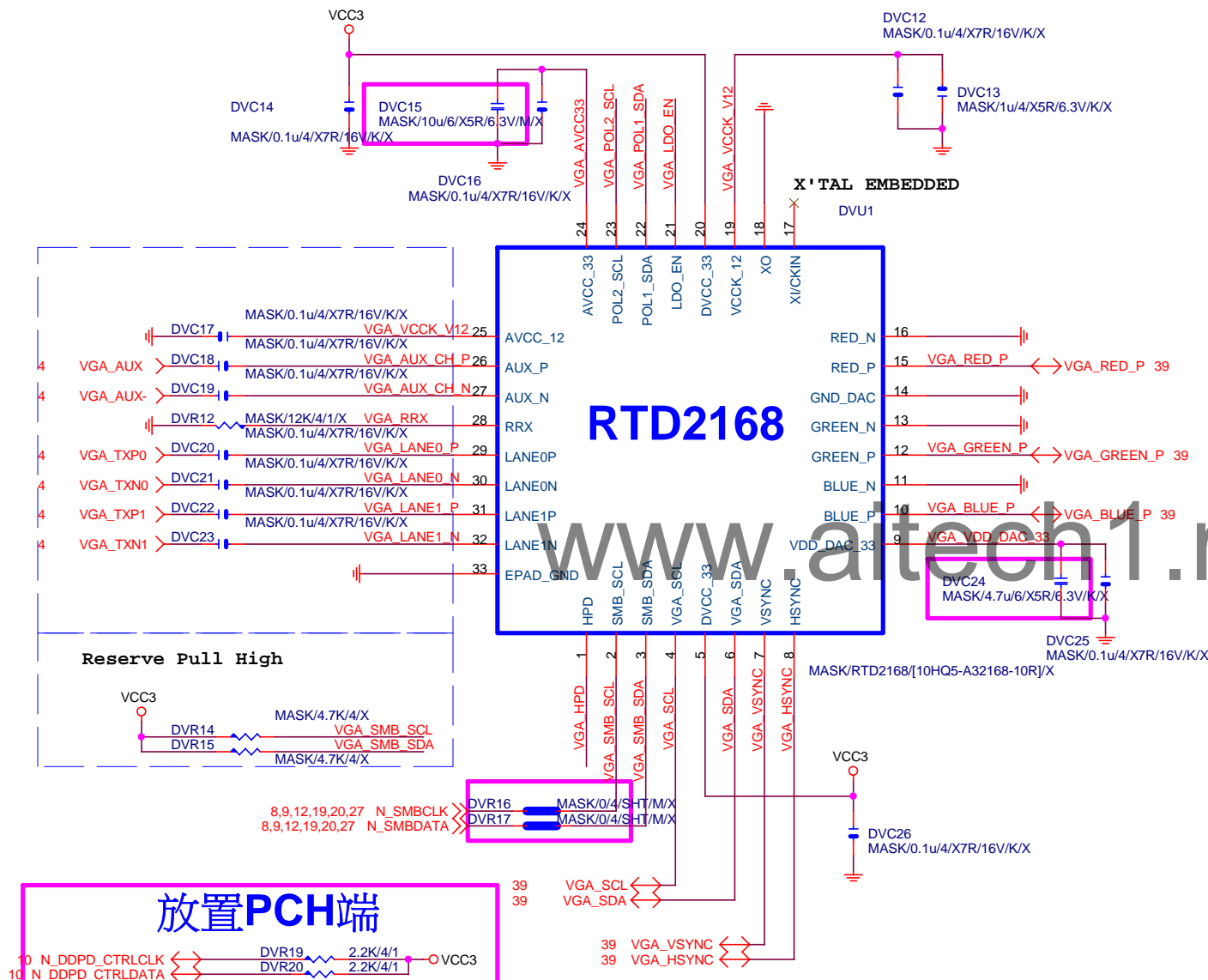
Document Number

GA-B250M-EVO

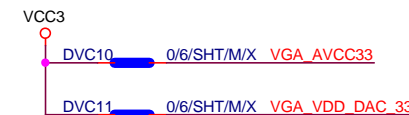
Rev	
1.0	

Date: Monday, November 14, 2016

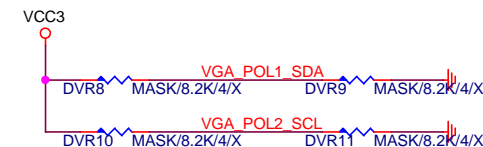
Sheet 37 of 53



POWER

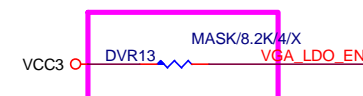


Power on latch



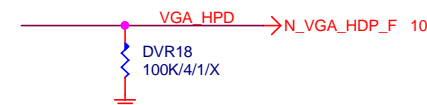
		POL1_SDA(PIN22)	
		0	1
POL2_SCL (PIN23)	0	X	EP MODE
	1	ROM ONLY MODE	EEPROM MODE

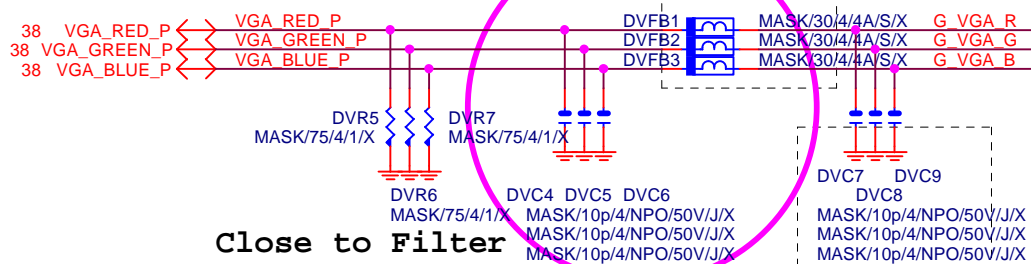
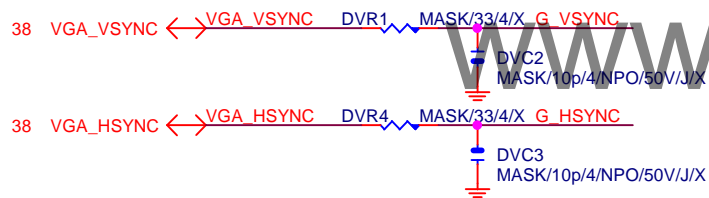
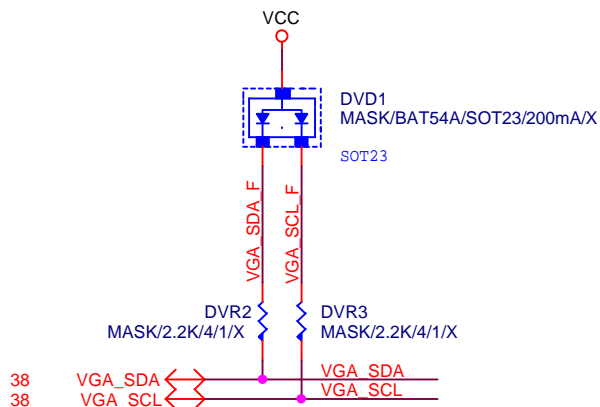
Embedded LDO



LDO_EN(PIN21)	
0	1
VCCK_V12 from External 1.2V	VCCK_V12 from Embedded LDO

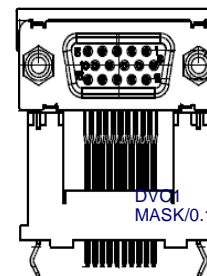
DP HPD



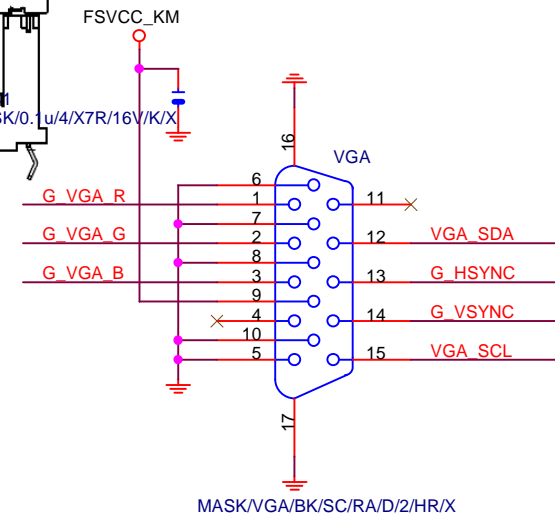


Close to Filter

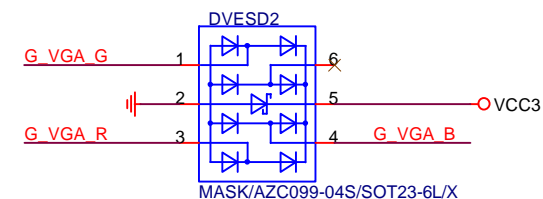
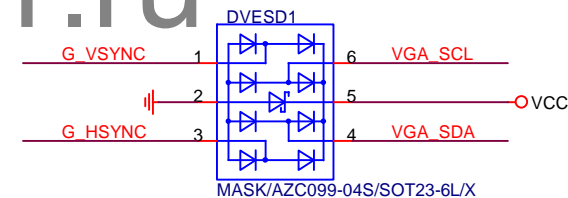
FOR EMI



架高型VGA (BLACK)

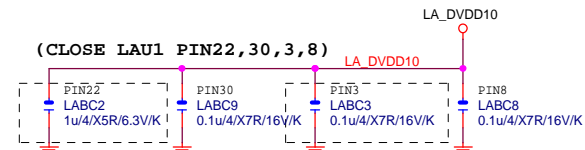
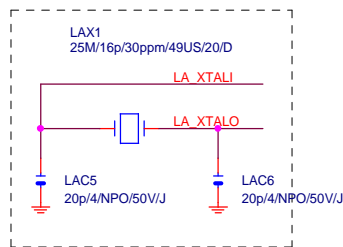


VGA ESD

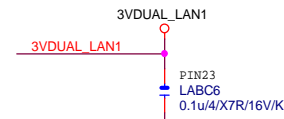
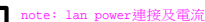


Gigabyte Technology
DP-VGA RTD2168

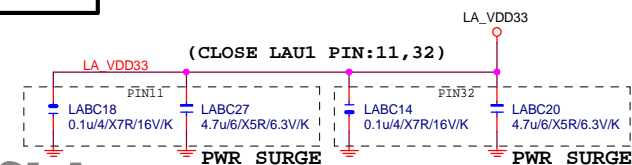
Title	Document Number	Rev
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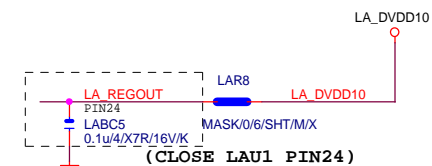
LABC2:1U CLOSE PIN22[REALTEK REQ]



(CLOSE LAU1 PIN23)



```
LABC18,27:CLOSE PIN11[REALTEK SURGE]
LABC14,20:CLOSE PIN32[REALTEK SURGE]
```



Gigabyte Technology

Realtek RTL8111G

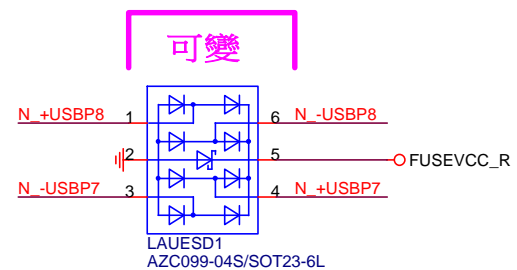
GA-B250M-EVO

Rev
1.0

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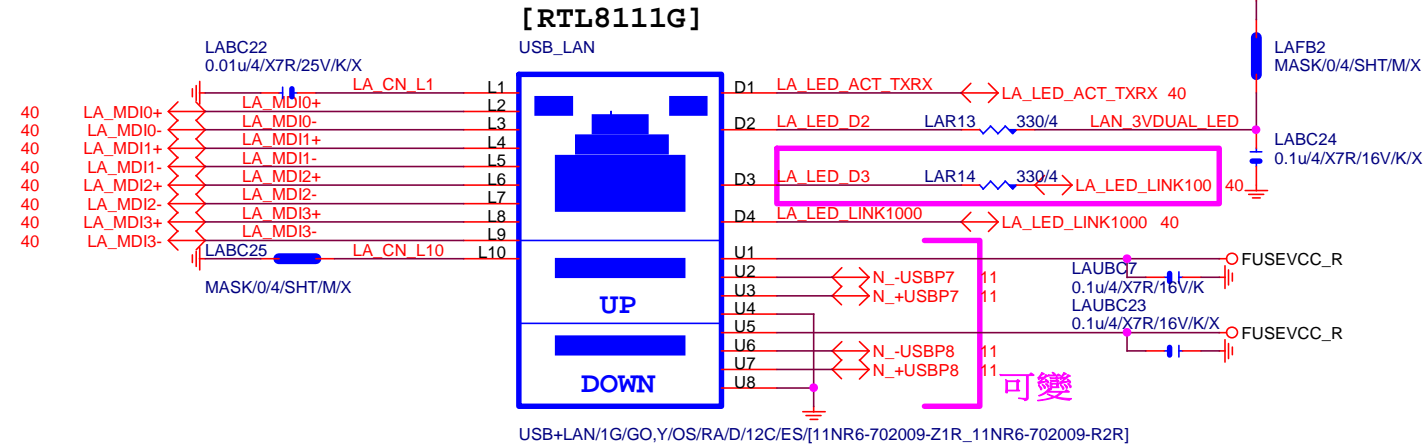
USB_LAN CONNECTOR R1.06

RMA ESD PROTECT note:可變更USB NAME



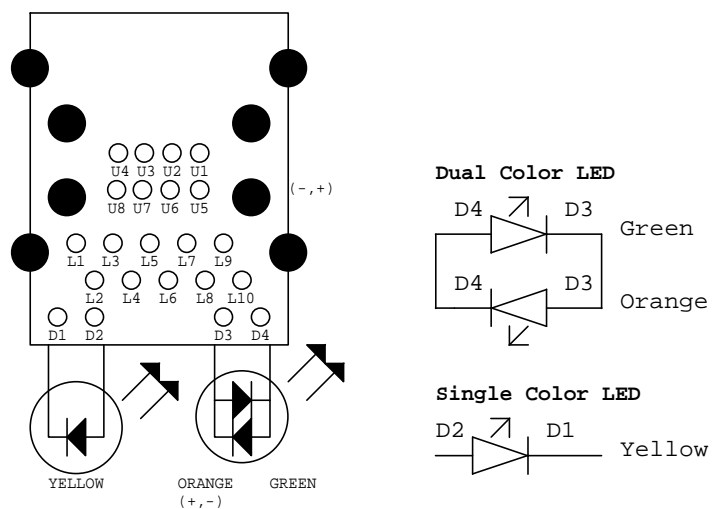
USB_LAN CONNECTOR

note:可變更USB NAME



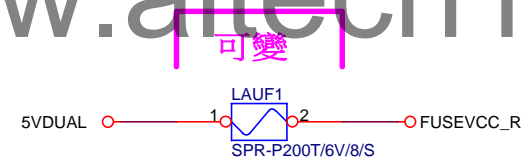
LA_MDI-->100歐姆:[20/4/8/4/20]

USB_LAN LAYOUT示意圖



USB POWER

note:可變更FUSE



Close to connector
USB_LAN 2-Port 2.0A
FUSE-0805

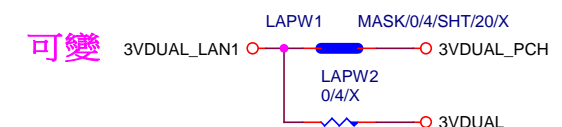
EMI SHORT PAD

PS:視EMI需求



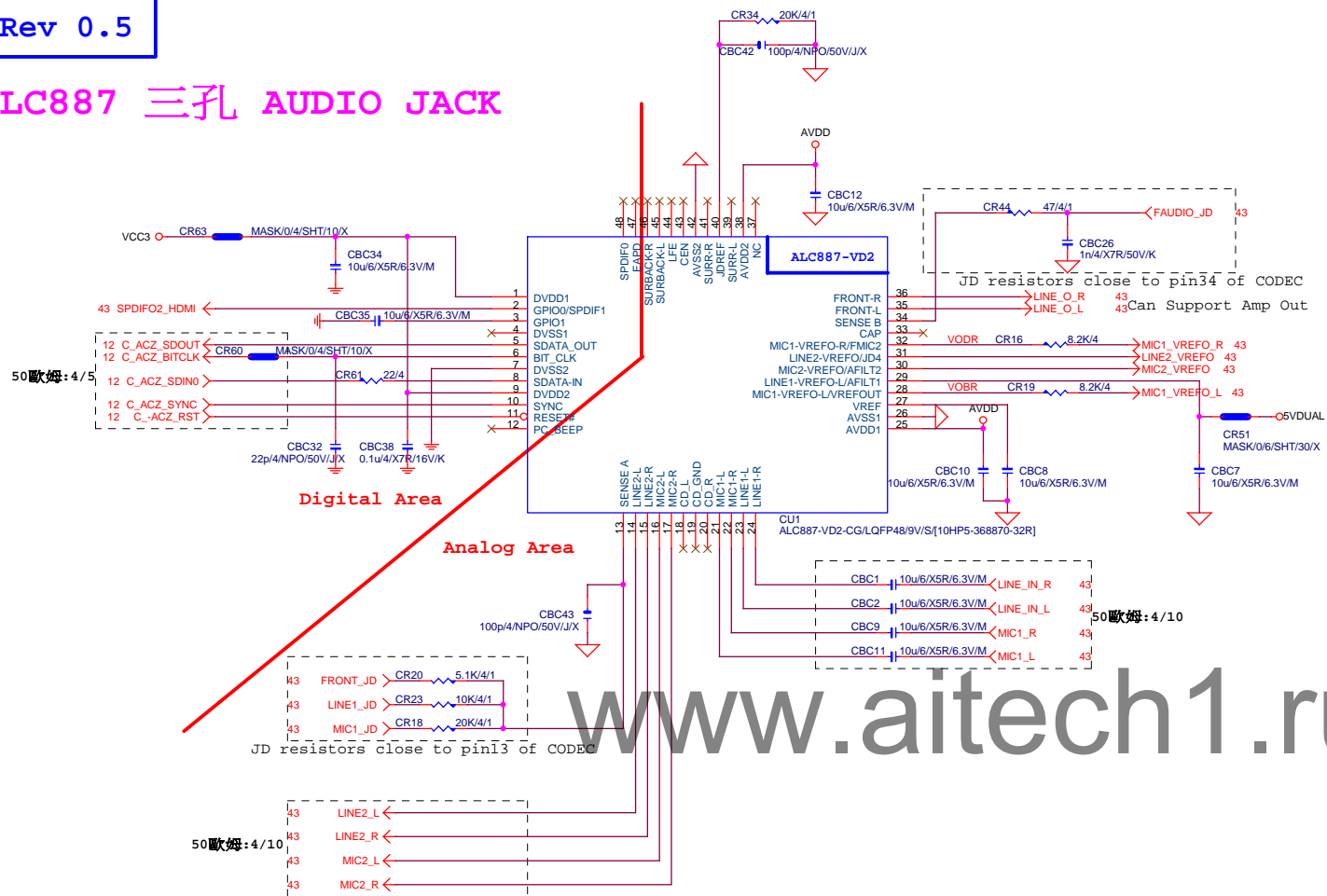
LAN POWER

note: lan power連接及電流



Gigabyte Technology			
LAN CONNECTOR-RTL811HS			
Title		Rev 1.0	
Size	Document Number	GA-B250M-EVO	
Custom			
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ALC887 三孔 AUDIO JACK



LAYOUT注意: 螺絲孔下GND方式

1. MH1空間夠, 下DGND
空間不夠, 改為Isolate
2. MH2一律改為Isolate

<input type="radio"/> MH1	<input type="radio"/> MH2
DGND	Isolate

LAYOUT注意: 要加

GND切割線



音效區域印刷



Gigabyte Technology

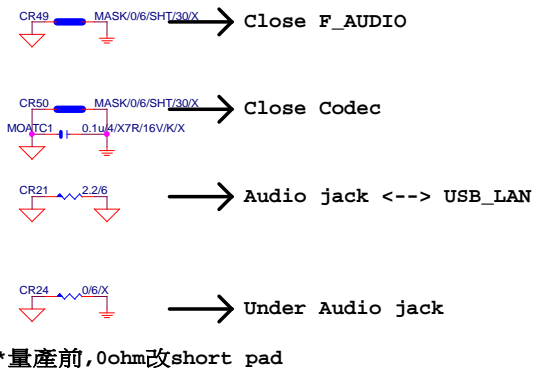
Title HD AUDIO ALC887

Size Custom Document Number GA-B250M-EVO

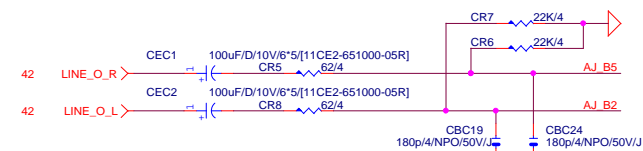
Rev 1.0

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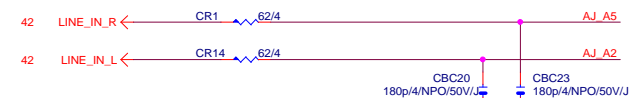
Rev 0.5



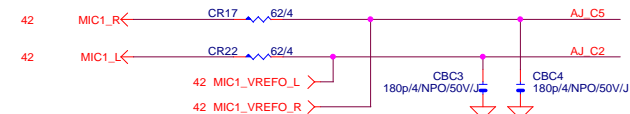
LINE-OUT



LINE-IN



MIC-IN

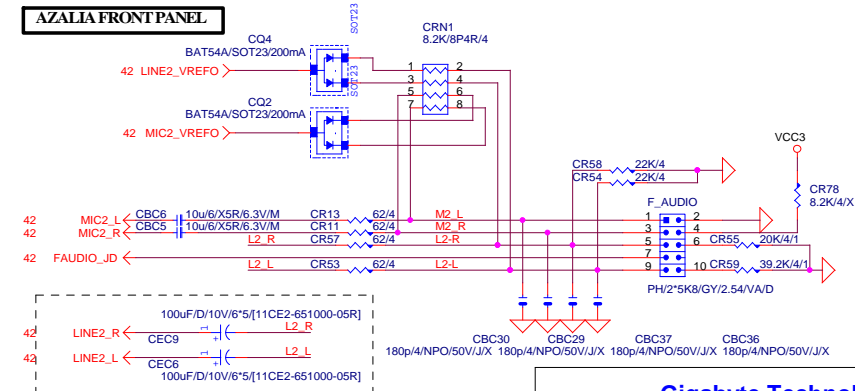


SURROUND

CEN/LFE

SURRBACK

AZALIA FRONT PANEL

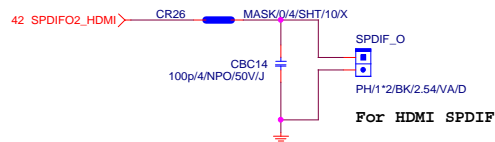


Gigabyte Technology

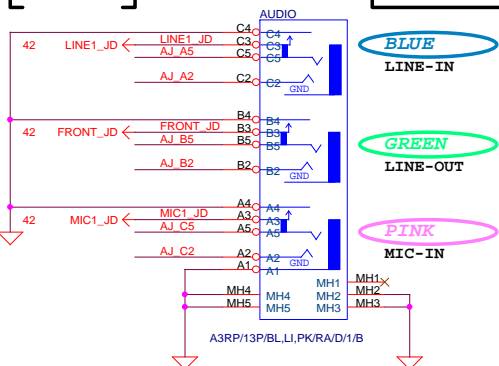
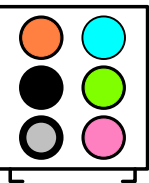
Title			
AUDIO JACK			Rev
GA-B250M-EVO			1.0
Size	Document Number		
Custom			
Date:	Monday, November 14, 2016	Sheet	43 of 53

SPDIF_IN

SPDIF_OUT



AZALIA JACK

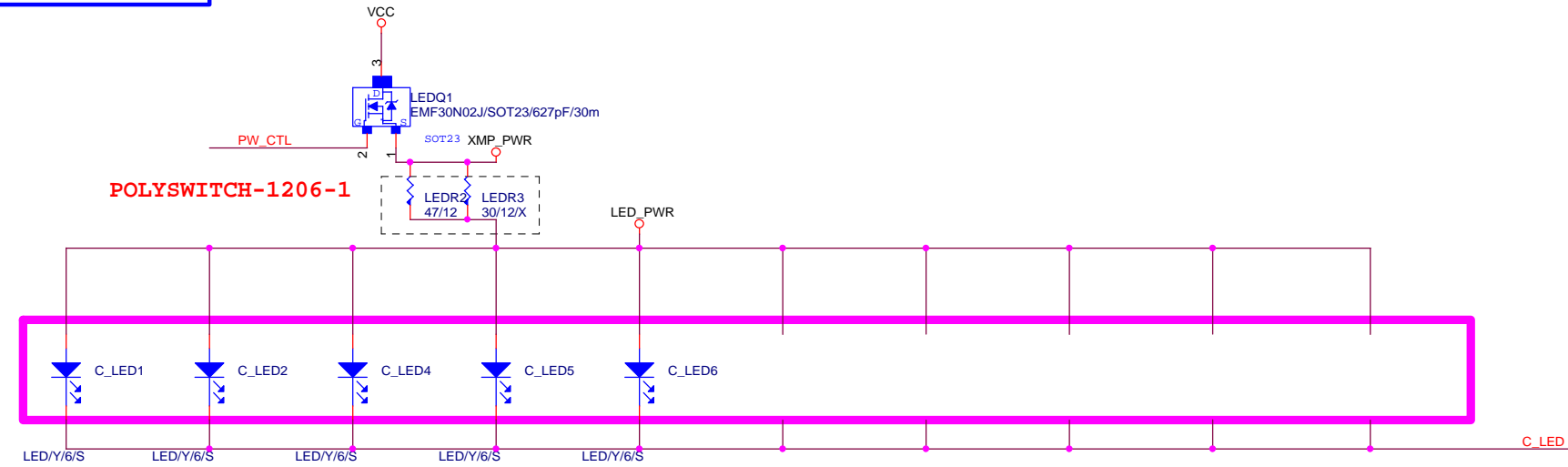


AZALIA JACK

BLUE
LINE-IN

GREEN
LINE-OUT

PINK
MIC-IN

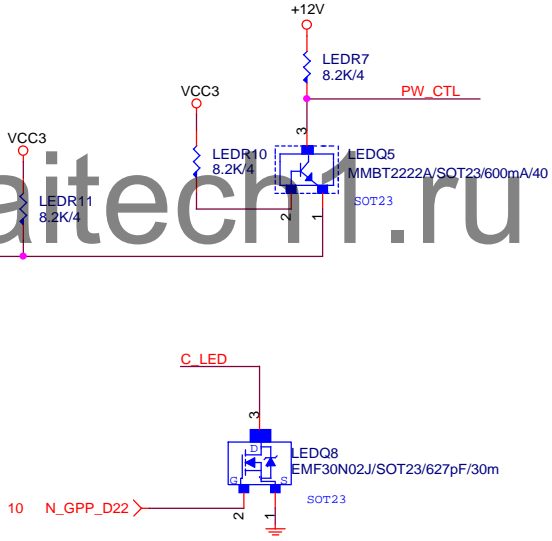


Ambient LED Control

	N_GPP_D22
Still Mode	H
OFF Mode	L

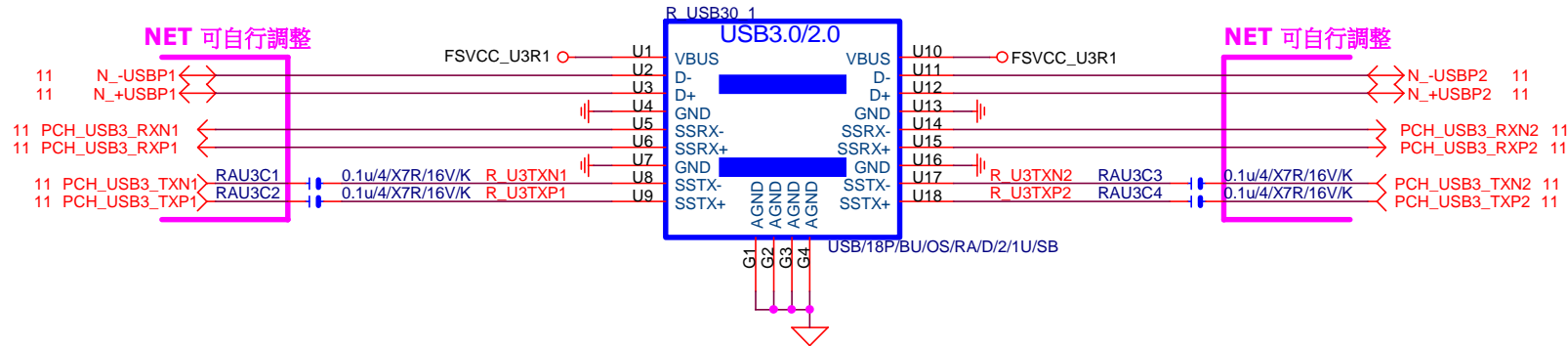
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N_GPP_D22
ON/OFF



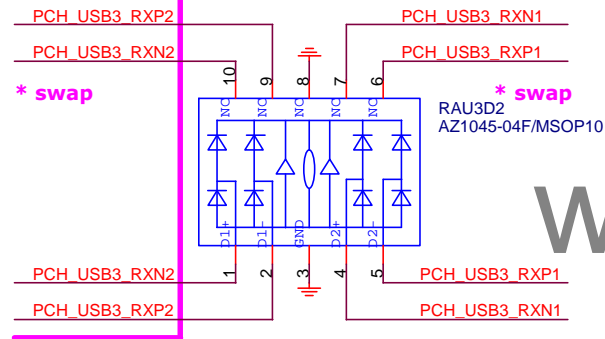
Rev: 0.53

ESD 可自行SWAP PIN ,CONN端 NET 名稱 不可

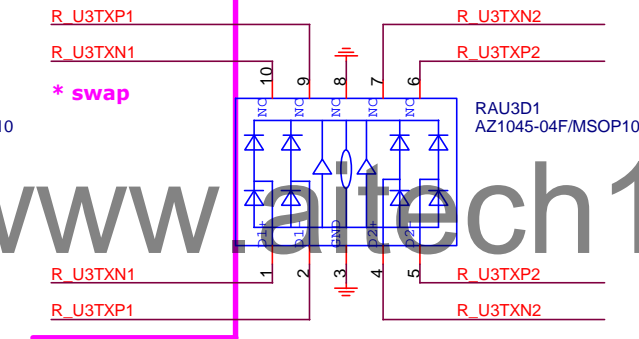


ESD

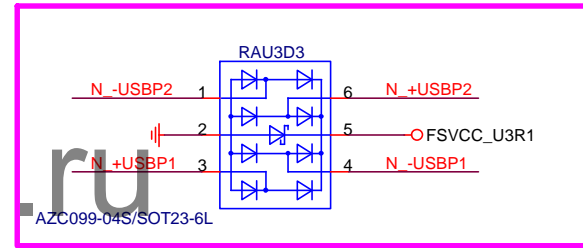
NET 可自行調整



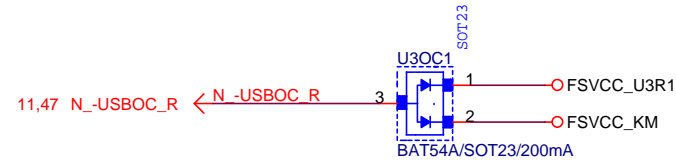
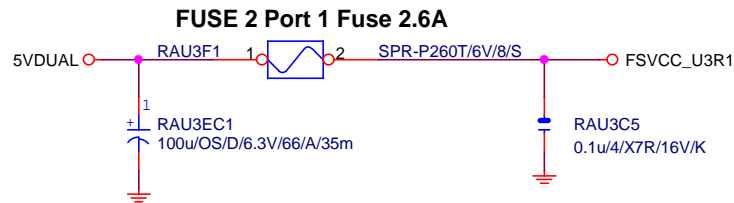
NET 可自行調整



NET 可自行調整

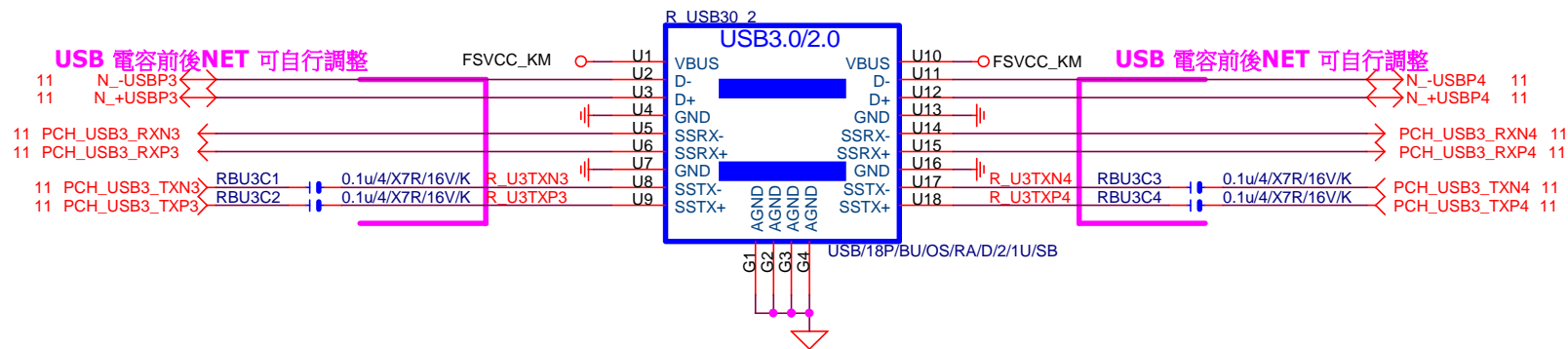


FUSE

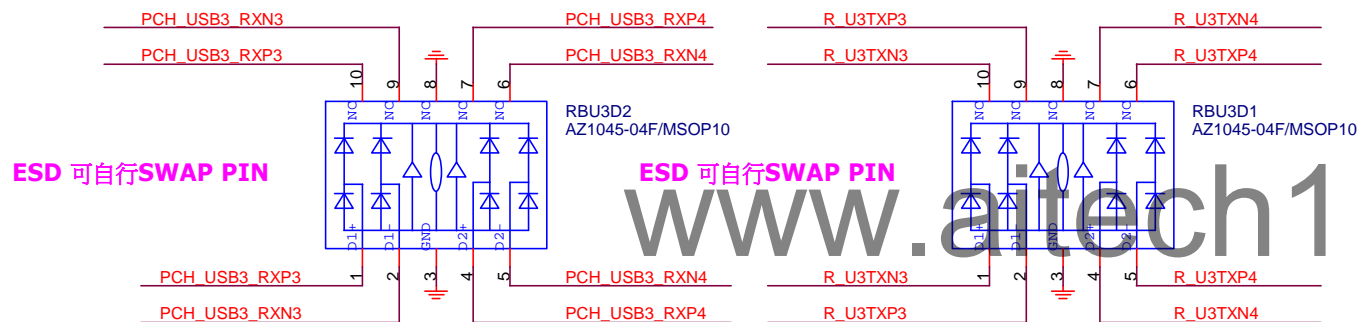


Gigabyte Technology

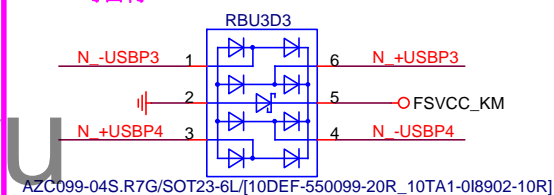
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R_USB30,USB_OC			
Size	Document Number	GA-B250M-EVO	
Custom			Rev 1.0
Date:	Monday, November 14, 2016	Sheet 45	of 53



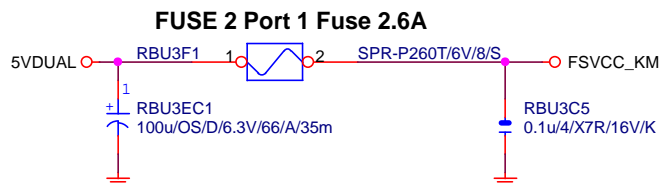
ESD



ESD 可自行SWAP PIN



FUSE



Gigabyte Technology

Title

R_USB30,USB_OC

Size
Custom

Document Number

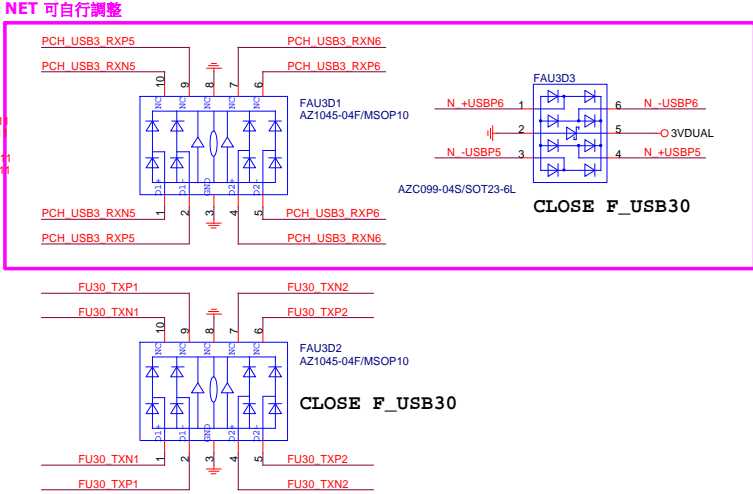
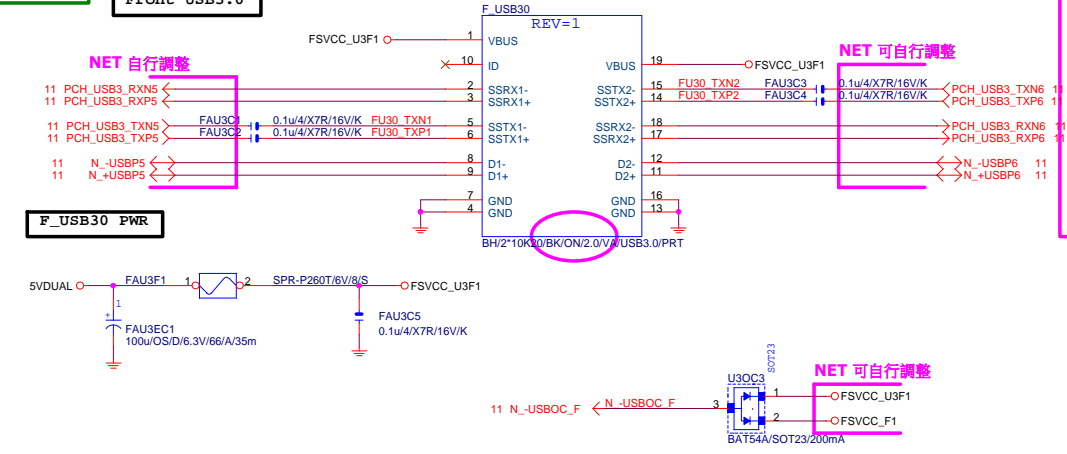
GA-B250M-EVO

Rev
1.0

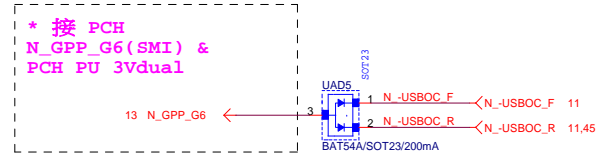
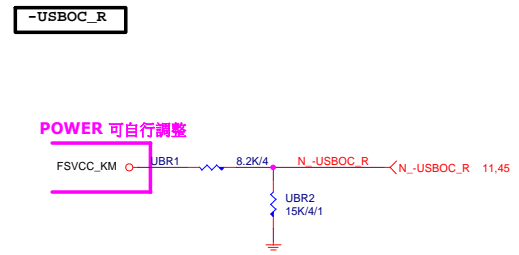
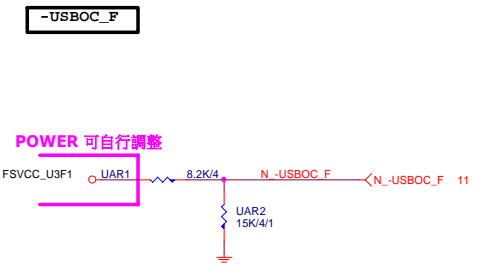
Date: Monday, November 14, 2016

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Front USB3.0



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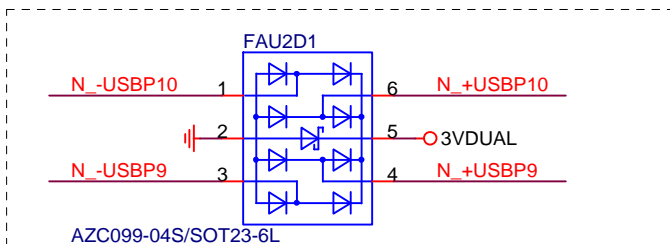
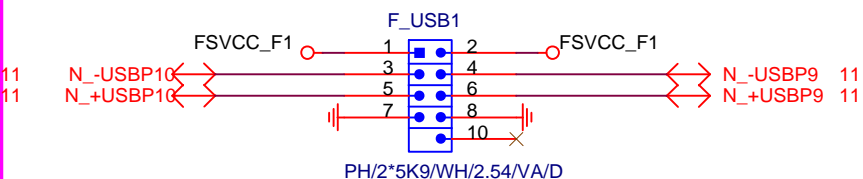


Rev: 0.7

FRONT USB1

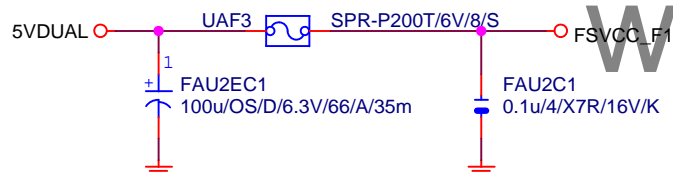
NET 可變

FUSB2X5-HS



Close to connector

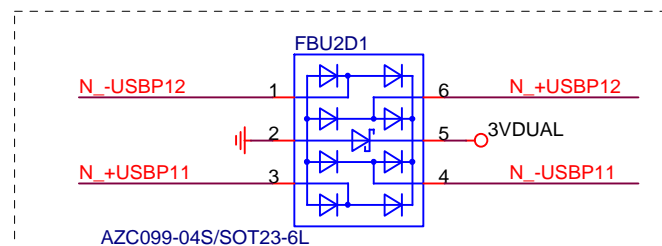
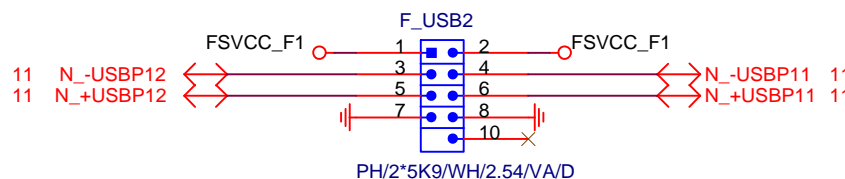
FUSE 2 Port 1 Fuse 2A



FRONT USB2

NET 可變

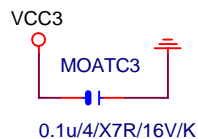
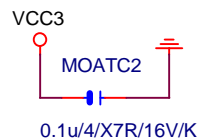
FUSB2X5-HS



Close to connector

FUSE 2 Port 1 Fuse 2A

F_USB 2.0 OC SIGNAL



Gigabyte Technology

Title

USB2.0

Size
A

Document Number

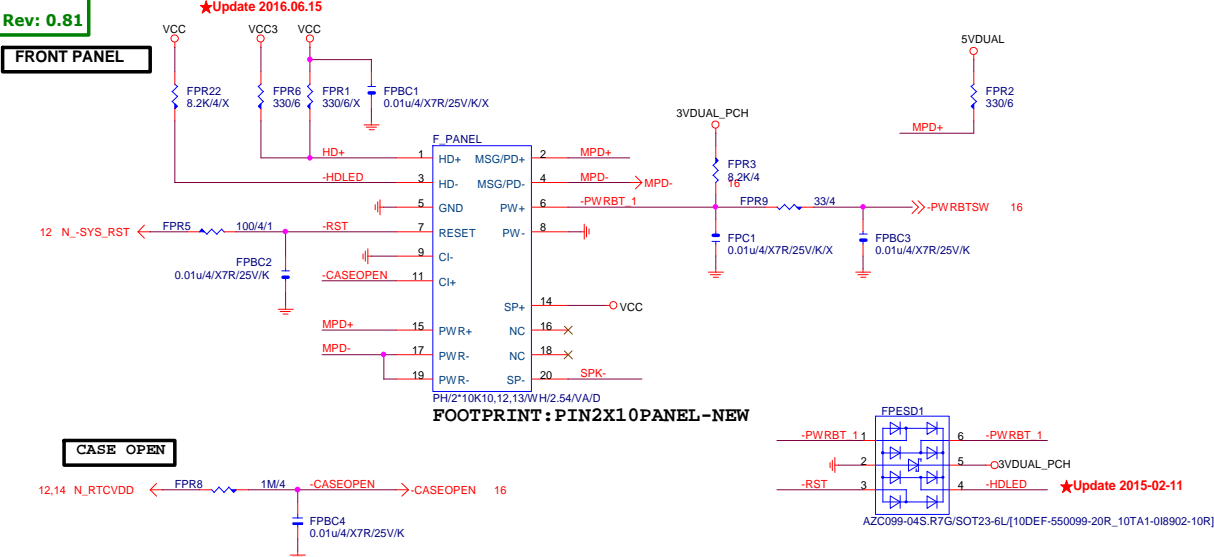
GA-B250M-EVO

Rev
1.0

Date: Monday, November 14, 2016

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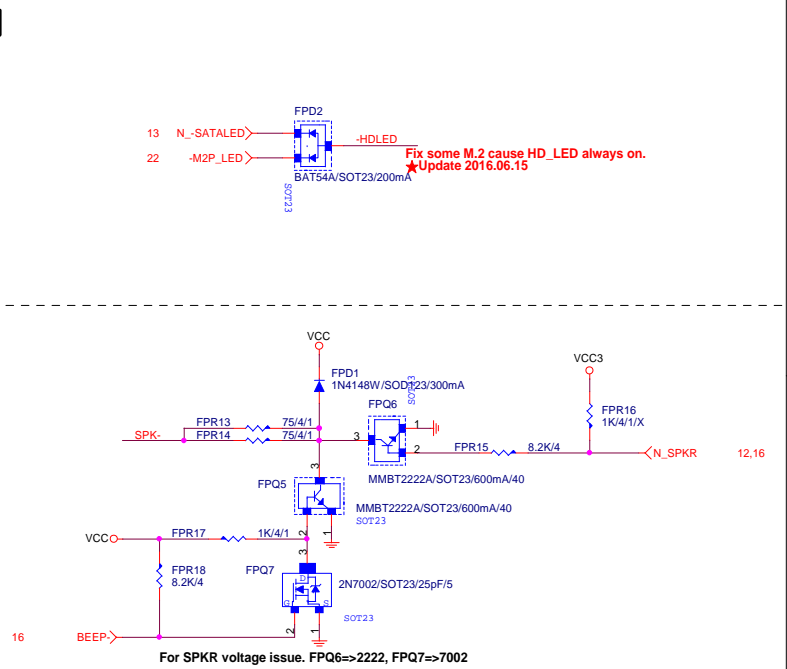
FRONT PANEL



FRONT PANEL SHORT

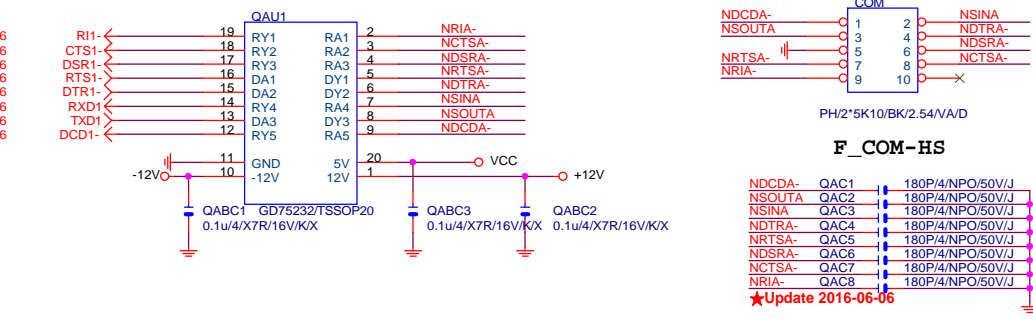
SATA/M.2 LED

SPKR

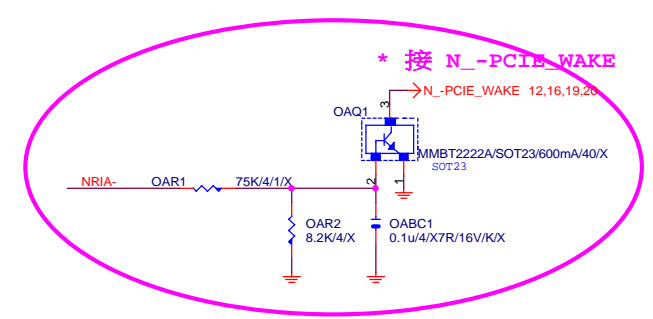


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COM PORT Rev: 0.7



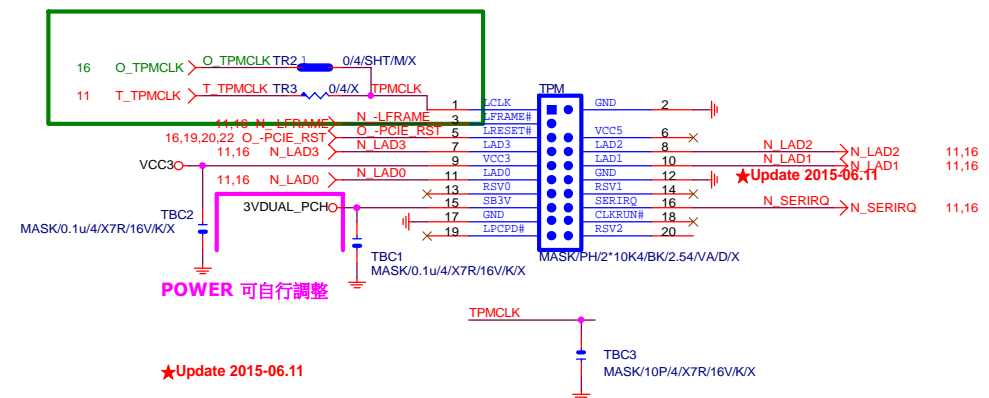
COM RI N/A



LPT PORT

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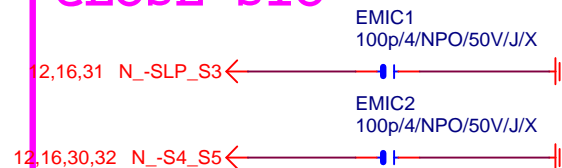
TPM CONNECT



Thunderbolt

★Update 2015-12-29

CLOSE SIO

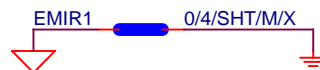


CLOSE PCH



close to PCH (NR17)

R_USB30_2 & DVI 之間



close to FRONT PANEL

GIGABYTE™

Title

EMI/ESDSize
A

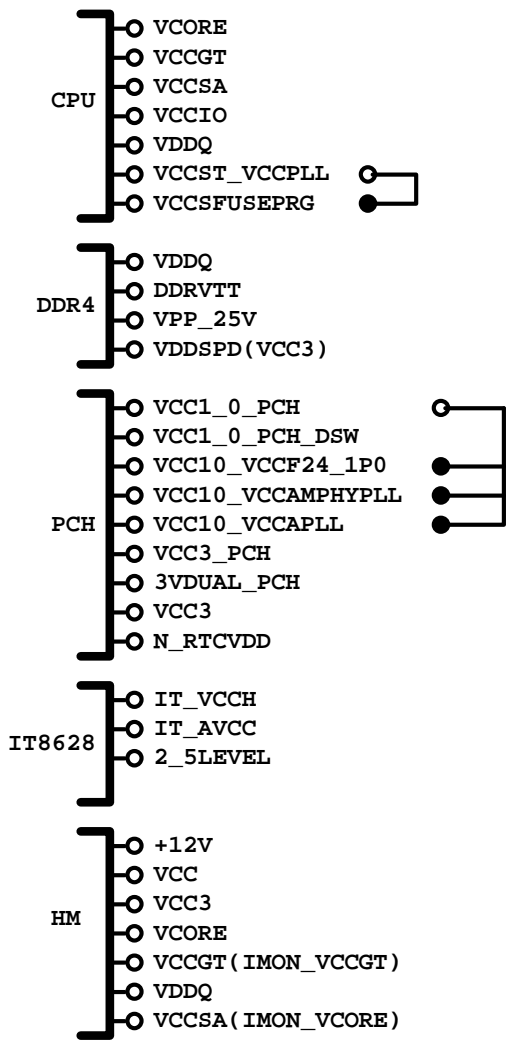
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GA-B250M-EVORev
1.0

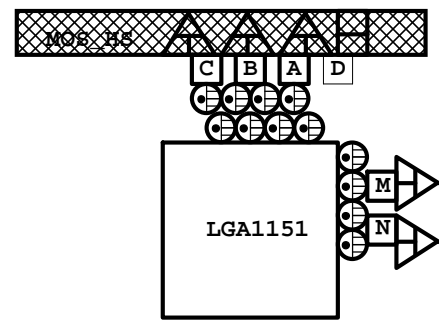
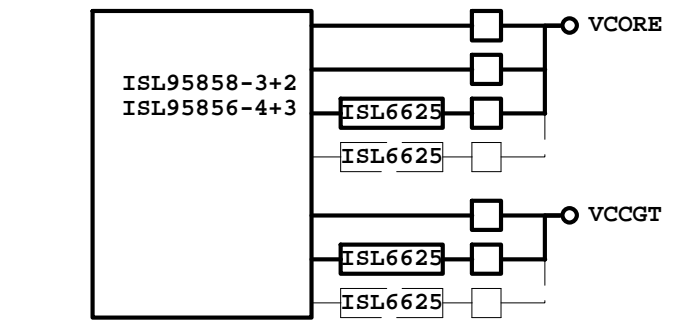
Date: Monday, November 14, 2016

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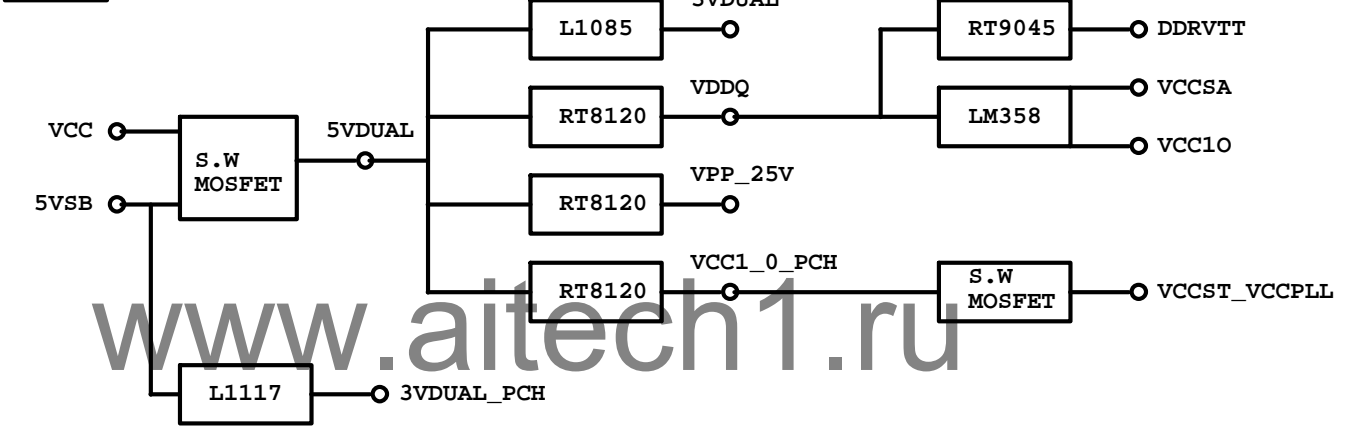
POWER BLOCK MAP



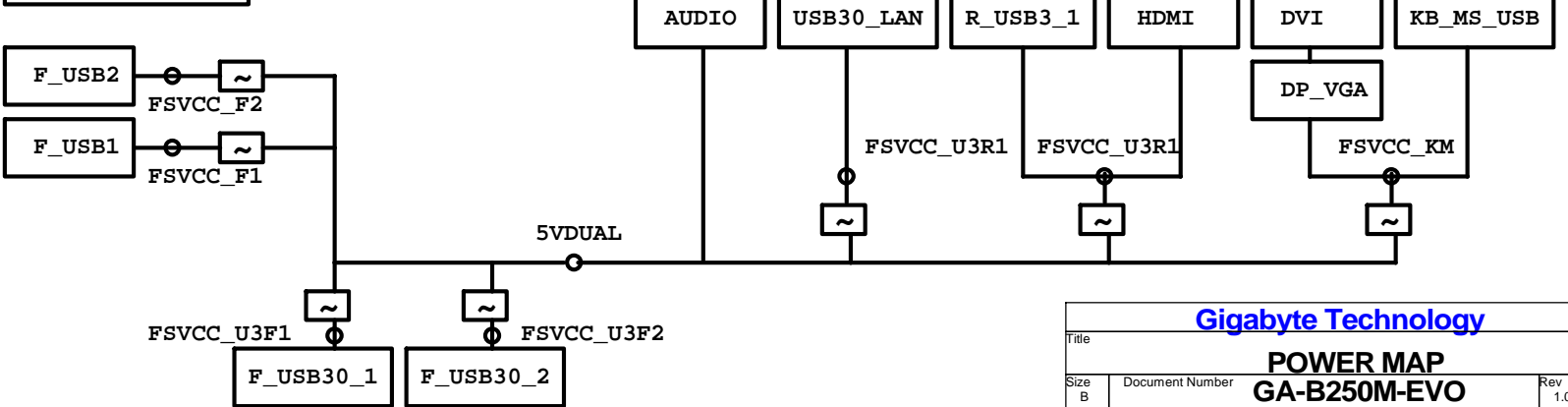
VCORE/VCCGT

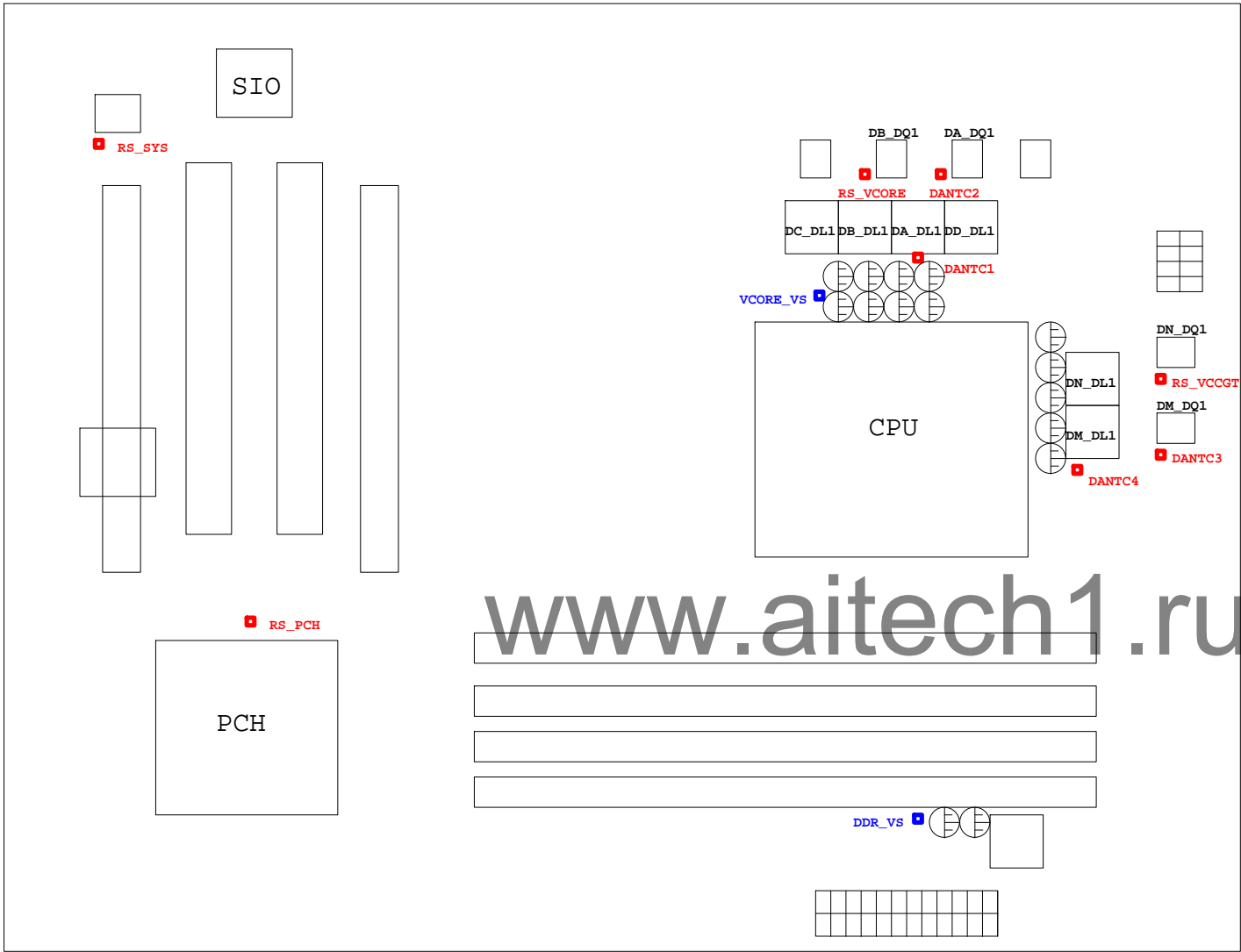


POWER



FUSE POWER F/R





熱敏電阻	擺放靠近位置	走線方式
DANTC1	DA_DL1	N/A
DANTC2	DA_DQ1	Differential
DANTC3	DM_DQ1	N/A
DANTC4	DM_DL1	Differential
RS_VCORE	DB_DQ1	N/A
RS_VCCGT	DN_DQ1	N/A
RS_PCH	PCH	N/A
RS_SYS	CU1	N/A